DATA SHEET

NEC

MOS FIELD EFFECT TRANSISTOR

2SK3113B

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3113B is N-channel MOS FET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

FEATURES

• Low on-state resistance

 $R_{DS(on)} = 4.4 \Omega MAX. (V_{GS} = 10 V, I_D = 1.0 A)$

• Low gate charge

 Q_G = 7.9 nC TYP. (V_{DD} = 450 V, V_{GS} = 10 V, I_D = 2.0 A)

- Gate voltage rating : ±30 V
- Avalanche capability ratings

<R> ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE	
2SK3113B-S15-AY Note	Pure Sn (Tin)	Tube 70 p/tube	TO-251 (MP-3-a) typ. 0.39 g	
2SK3113B(1)-S27-AY Note		Tube 75 p/tube	TO-251 (MP-3-b) typ. 0.34 g	
2SK3113B-ZK-E1-AY Note		Tape 2500 p/reel		
2SK3113B-ZK-E2-AY Note			TO-252 (MP-3ZK) typ. 0.27 g	

Note Pb-free (This product does not contain Pb in external electrode.)

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

•	,			
Drain to Source Voltage (VGs = 0 V)	VDSS	600	V	
Gate to Source Voltage (VDs = 0 V)	Vgss	±30	V	
Drain Current (DC) (Tc = 25°C)	ID(DC)	±2.0	А	
Drain Current (pulse) ^{Note1}	D(pulse)	±8.0	А	
Total Power Dissipation (Tc = 25° C)	P _{T1}	20	W	
Total Power Dissipation $(T_A = 25^{\circ}C)^{Note2}$	Pt2	1.0	W	
Channel Temperature	Tch	150	°C	
Storage Temperature	Tstg	–55 to +150	°C	
Single Avalanche Current Note3	las	2.0	Α	
Single Avalanche Energy Note3	Eas	2.7	mJ	

(TO-251)



(TO-252)



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Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on glass epoxy board of 40 mm × 40 mm × 1.6 mm

3. Starting T_{ch} = 25°C, V_{DD} = 150 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 600 V, V _{GS} = 0 V			100	μA
Gate Leakage Current	lgss	V _{GS} = ±30 V, V _{DS} = 0 V			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.5		3.5	v
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 1.0 A	0.5	0.9		S
Drain to Source On-state Resistance ^{Note}	RDS(on)	Vgs = 10 V, Id = 1.0 A		3.2	4.4	Ω
Input Capacitance	Ciss	V _{DS} = 10 V		290		pF
Output Capacitance	Coss	V _{GS} = 0 V		75		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		7		pF
Turn-on Delay Time	td(on)	V _{DD} = 150 V, I _D = 1.0 A		10.5		ns
Rise Time	tr	V _{GS} = 10 V		4.8		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		15.8		ns
Fall Time	tr	RL = 10 Ω		10.5		ns
Total Gate Charge	QG	V _{DD} = 450 V		7.9		nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V		2.7		nC
Gate to Drain Charge	Qgd	I _D = 2.0 A		3.2		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 2.0 A, VGS = 0 V		0.8		V
Reverse Recovery Time	trr	IF = 2.0 A, VGS = 0 V		190		ns
Reverse Recovery Charge	Qrr	di/dt = 50 A/ <i>µ</i> s		500		nC

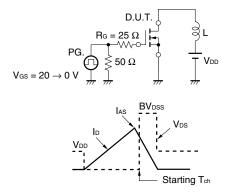
Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

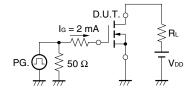
TEST CIRCUIT 2 SWITCHING TIME

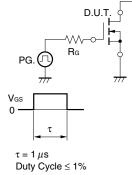
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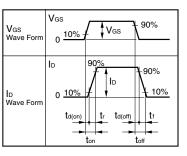
VDD



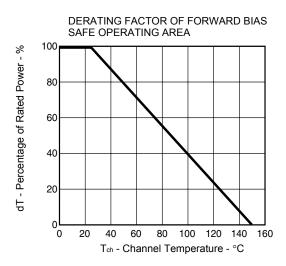
TEST CIRCUIT 3 GATE CHARGE

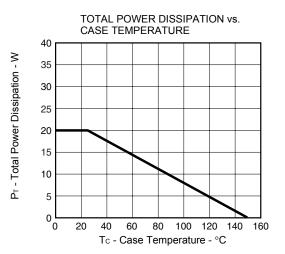




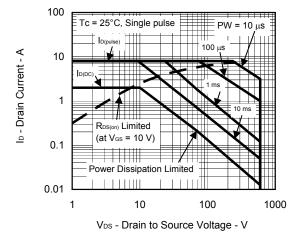


TYPICAL CHARACTERISTICS (TA = 25°C)

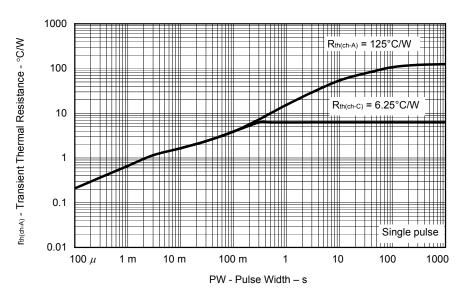




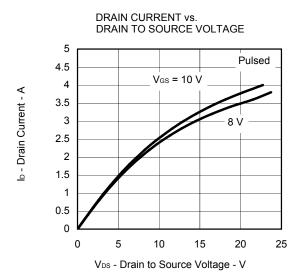
FORWARD BIAS SAFE OPERATING AREA



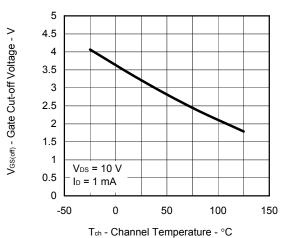


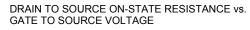


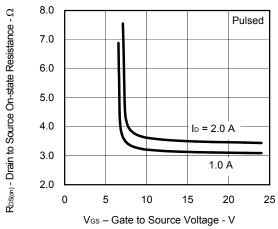
Data Sheet D18061EJ3V0DS



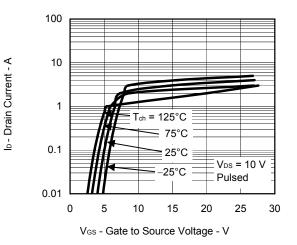




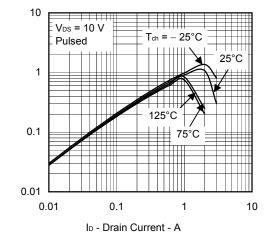


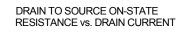


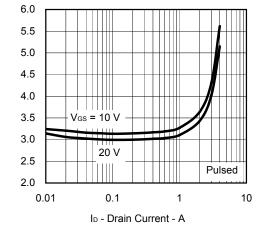




FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

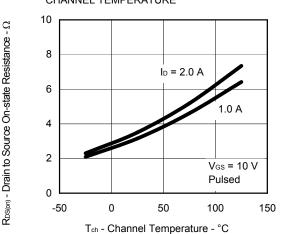






| y_{fs} | - Forward Transfer Admittance - S

 $\mathsf{R}^{\mathsf{DS}(m)}$ - Drain to Source On-state Resistance - Ω



CAPACITANCE vs. DRAIN TO

С

C rss

10

C oss

SOURCE VOLTAGE

1000

100

10

1

0.1

V_{GS} = 0 V

= 1 MHz

1

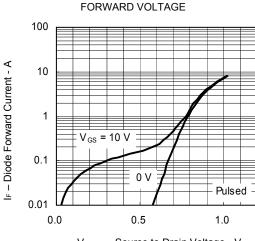
VDS - Drain to Source Voltage - V

REVWESE RECOVERY TIME vs.

Coss, Crss - Capacitance - pF

C_{iss},

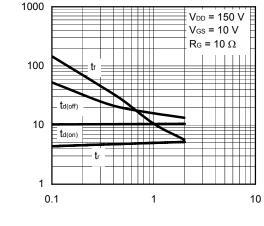
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



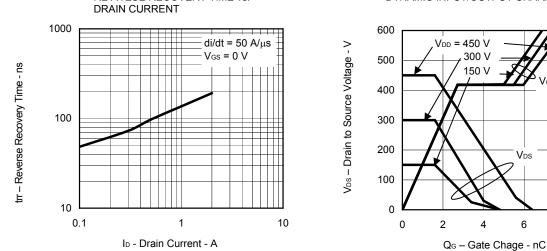
SOURCE TO DRAIN DIODE

VF(S-D) - Source to Drain Voltage - V

SWITCHING CHARACTERISTICS



ID - Drain Current - A



100

DYNAMIC INPUT/OUTPUT CHARACTERISTICS

Data Sheet D18061EJ3V0DS

(d(on), tr, td(off), tr - Switching Time - ns

10

9

8

7

6

5 4

3

2

1

0

10

ID = 2.0 A

8

V_{GS} – Gate to Source Voltage - V

 $V_{\text{DD}} = 150 \text{ V}$

 $V\text{Gs}=20\rightarrow 0~V$

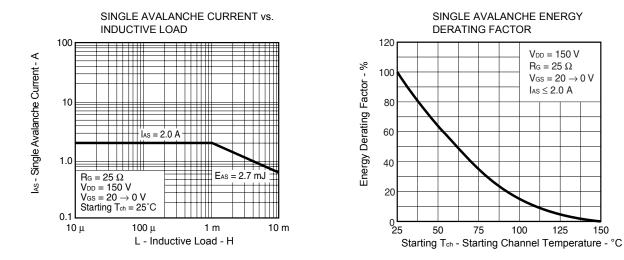
125

150

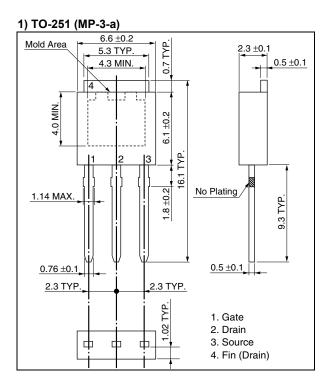
100

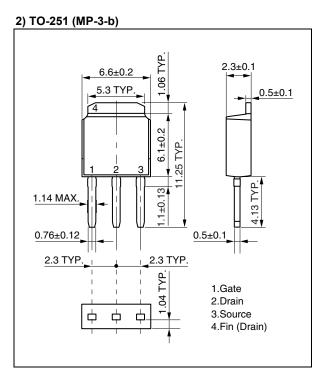
 $R_G = 25 \ \Omega$

 $I_{\text{AS}} \leq 2.0 \ A$

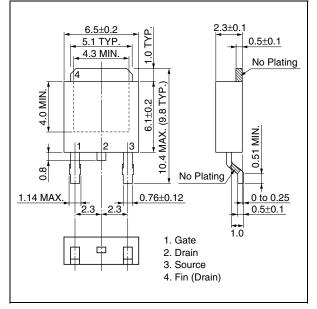


<R> PACKAGE DRAWINGS (Unit: mm)

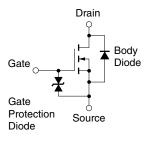




3) TO-252 (MP-3ZK)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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