

# MOS FIELD EFFECT TRANSISTOR 2SK3811

## SWITCHING N-CHANNEL POWER MOS FET

#### **DESCRIPTION**

The 2SK3811 is N-channel MOS Field Effect Transistor designed for high current switching applications.

#### ORDERING INFORMATION

PART NUMBER	PACKAGE		
2SK3811-ZP	TO-263 (MP-25ZP)		

#### **FEATURES**

• Super low on-state resistance  $R_{DS(on)}$  = 1.8 m $\Omega$  MAX. (VGS = 10 V, ID = 55 A)

• High Current Rating: ID(DC) = ±110 A

(TO-263)



### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V <sub>GS</sub> = 0 V)	VDSS	40	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±110	Α
Drain Current (pulse) Note1	D(pulse)	±440	Α
Total Power Dissipation (Tc = 25°C)	P <sub>T1</sub>	213	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T2</sub>	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	518	mJ
Repetitive Avalanche Current Note3	Iar	72	Α
Repetitive Avalanche Energy Note3	Ear	518	mJ

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

**2.** Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 20 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V, L = 100  $\mu$ H

3. Rg = 25  $\Omega$ , Tch(peak)  $\leq 150^{\circ}$ C

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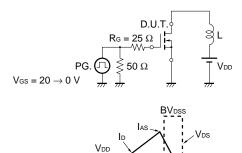


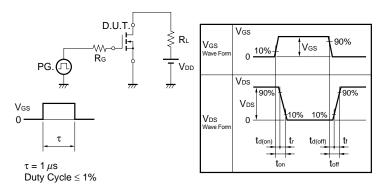
**ELECTRICAL CHARACTERISTICS (TA = 25°C)** 

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	Igss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±100	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 55 A	45	89		S
Drain to Source On-state Resistance Note	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 55 A		1.4	1.8	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		17700		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		2200		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1300		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 55 A		54		ns
Rise Time	tr	V <sub>GS</sub> = 10 V		140		ns
Turn-off Delay Time	<b>t</b> d(off)	R <sub>G</sub> = 0 Ω		130		ns
Fall Time	tr			21		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = 32 V		260		nC
Gate to Source Charge	Qgs	V <sub>GS</sub> = 10 V		57		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 110 A		83		nC
Body Diode Forward Voltage Note	V <sub>F(S-D)</sub>	I <sub>F</sub> = 110 A, V <sub>GS</sub> = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	I <sub>F</sub> = 110 A, V <sub>GS</sub> = 0 V		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		80		nC

Note Pulsed

### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**





**TEST CIRCUIT 2 SWITCHING TIME** 

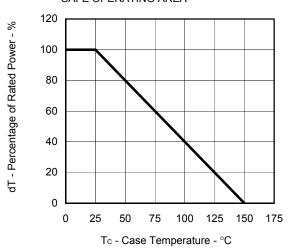
### **TEST CIRCUIT 3 GATE CHARGE**

PG. 
$$\bigcirc$$
 S 50  $\Omega$   $\bigcirc$  RL

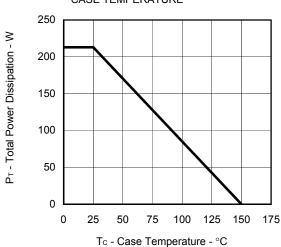
Starting Tch

### TYPICAL CHARACTERISTICS (TA = 25°C)

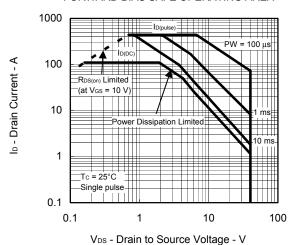
### DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



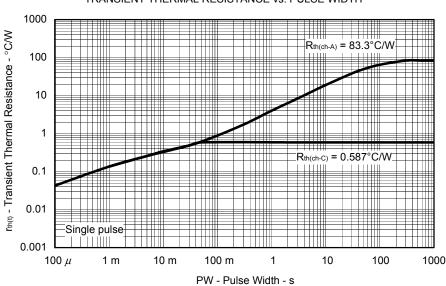
### TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



#### FORWARD BIAS SAFE OPERATING AREA

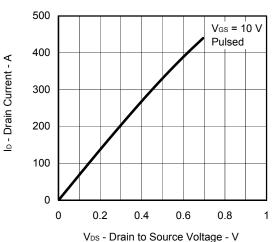


### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

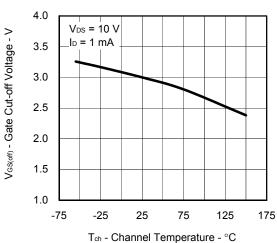


3

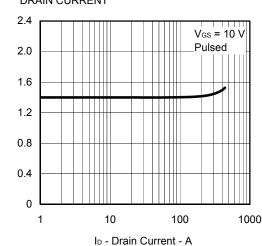
#### DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



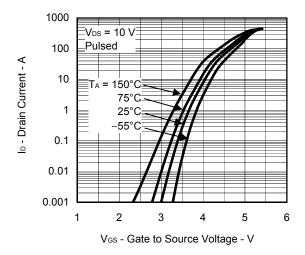
### GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



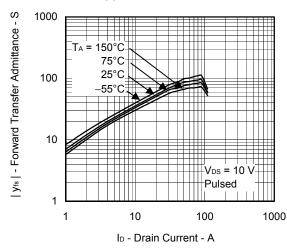
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



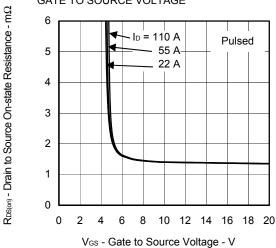
#### FORWARD TRANSFER CHARACTERISTICS



### FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

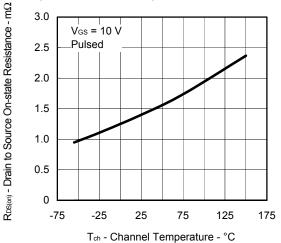


### DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

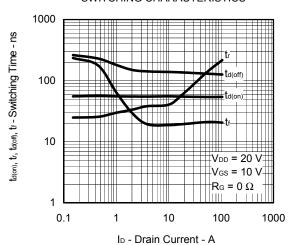


R<sub>DS(on)</sub> - Drain to Source On-state Resistance - mΩ

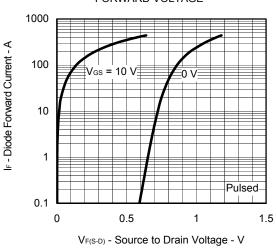
### DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



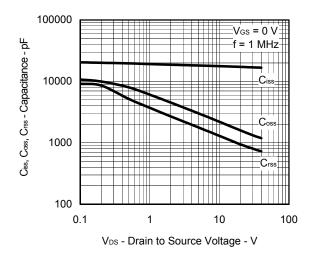
### SWITCHING CHARACTERISTICS



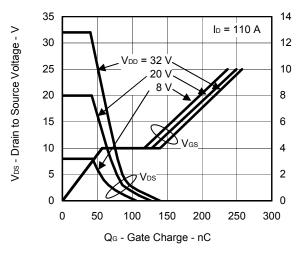
### SOURCE TO DRAIN DIODE FORWARD VOLTAGE



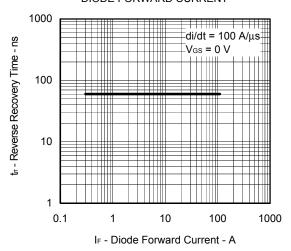
#### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



#### DYNAMIC INPUT/OUTPUT CHARACTERISTICS

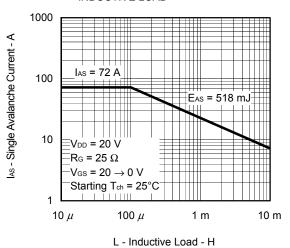


### REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT

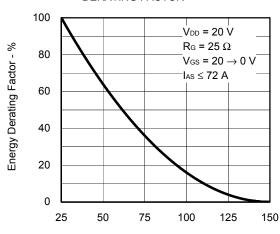


Ves - Gate to Source Voltage - V

### SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



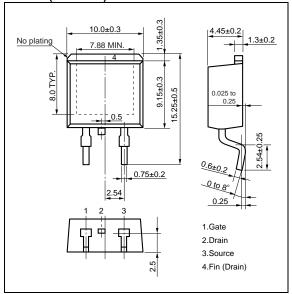
### SINGLE AVALANCHE ENERGY DERATING FACTOR



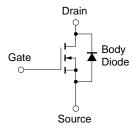
Starting Tch - Starting Channel Temperature - °C

### PACKAGE DRAWING (Unit: mm)

### TO-263 (MP-25ZP)



### **EQUIVALENT CIRCUIT**



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

Data Sheet D16737EJ1V0DS 7

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