



AL4V93/95

16K, 64K-Bit Line FIFO

Applications

- Multimedia System
- Video line capture
- TBC(Time Base Corrector)
- Hard Disk cache memory
- Anti-skip audio data buffer

Description

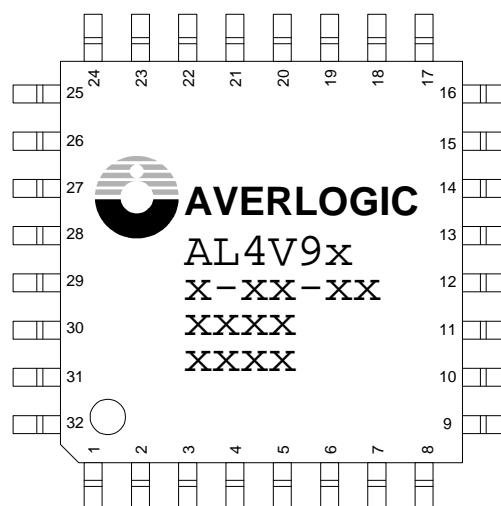
The AL4V93/95 series memory products are high-performance, low-power 9-bit read/write FIFO (First-In-First-Out) memory chips. They are specially designed to buffer high speed streaming data for a wide range of data buffering applications, such as optical disk data caching, video line data buffering.

Features

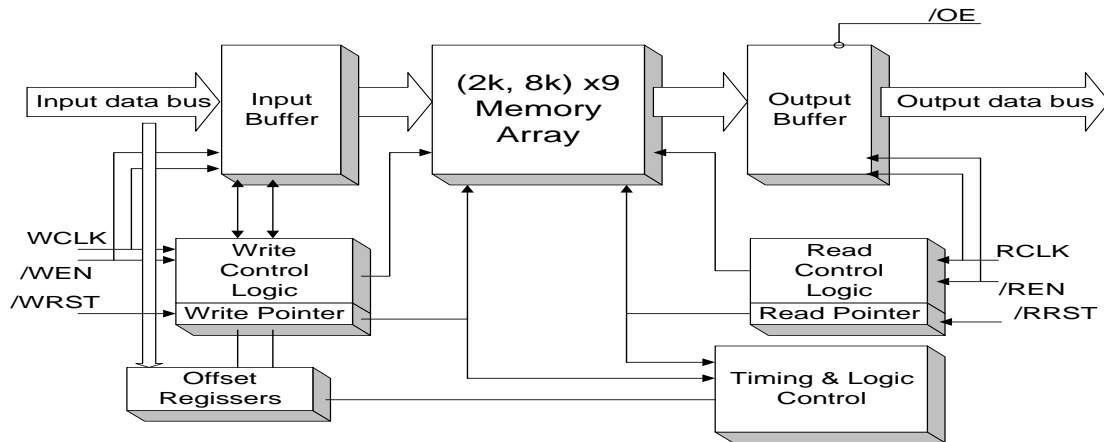
- High performance, low-power, FIFO(First-In First-Out) memory
- 2K x9 bit I/O port (AL4V93)
- 8K x9 bit I/O port (AL4V95)
- High clock speed (100MHz)
- Fully independent read/write access
- Output enable control (data skipping)
- 3.3V±10% power supply with 5V tolerant
- Standard 32-pin TQFP

Ordering Information

Part number	AL4V93, AL4V95
Package	32-pin plastic TQFP and PLCC
Power Supply	+3.3V±10%



TQFP PACKAGE TOP VIEW



AL4V9x FIFO Block Diagram

The 9-bit input and output ports operate independently at a maximum speed of 100MHz. The built-in address decoder and pointer managing circuits provide a straightforward bus interface to serially read/write memory that reduces inter-chip design efforts. The AL4V9x embedded memory array and high performance process technologies with extended controller functions (read skip, fixed and programmable status flags... etc.) offer flexible memory management.

The input data is synchronous with a free-running clock (WCLK), and input-enable pins (\overline{WEN}). Data is written into the FIFO on every clock when enable pins are asserted. The output is synchronous with the other free-running clock (RCLK) and enables (\overline{REN}). An Output Enable pin (\overline{OE}) is provided at the read port for tri-state control of the output port.

The AL4V9x series are operating in 3.3V power supply with 5V signal tolerant input. These chips are available as a 32-pin TQFP Package.

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