

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
40V	5.5mΩ @ V _{GS} = 10V	71A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power management functions
- DC-DC converters

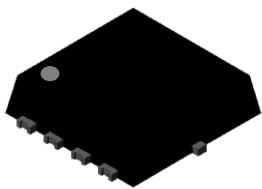
Features and Benefits

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- Low R_{DS(ON)} – Ensures On-State Losses are Minimized
- Excellent Q_{gd} x R_{DS(ON)} Product (FOM)
- Wettable Flank for Improved Optical Inspection
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

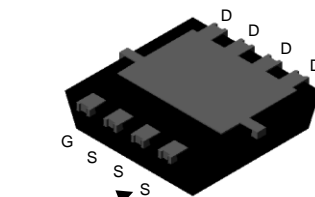
Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.072 grams (Approximate)

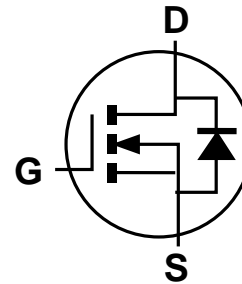
PowerDI3333-8 (SWP) (Type UX)



Top View



Pin 1
Bottom View



Equivalent Circuit

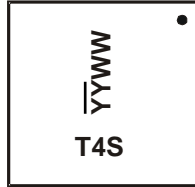
Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMTH45M5SFVW-7	PowerDI3333-8 (SWP) (Type UX)	2,000	Tape & Reel
DMTH45M5SFVW-13	PowerDI3333-8 (SWP) (Type UX)	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

PowerDI3333-8 (SWP) (Type UX)



T4S = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 23 = 2023)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	40	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 5), V _{GS} = 10V	I _D	T _C = +25°C	71
		T _C = +100°C	50
Continuous Drain Current (Note 6), V _{GS} = 10V	I _D	T _A = +25°C	18
		T _A = +100°C	13
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	284	A
Maximum Continuous Body Diode Forward Current (Note 5)	I _S	71	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	284	A
Avalanche Current, L = 0.1mH	I _{AS}	20.5	A
Avalanche Energy, L = 0.1mH	E _{AS}	21	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P _D	3.5	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	42	°C/W
Total Power Dissipation (Note 5)	P _D	51	W
Thermal Resistance, Junction to Case (Note 5)	R _{θJC}	2.9	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Notes: 5. Thermal resistance from junction to soldering point (on the exposed drain pad).
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 32V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2.0	—	3.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	4.2	5.5	mΩ	V _{GS} = 10V, I _D = 25A
Diode Forward Voltage	V _{SD}	—	0.85	1.2	V	V _{GS} = 0V, I _S = 25A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	1083	—	pF	V _{DS} = 20V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	621	—		
Reverse Transfer Capacitance	C _{rss}	—	21	—		
Gate Resistance	R _g	—	1.5	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	13.2	—	nC	V _{DS} = 20V, I _D = 25A, V _{GS} = 10V
Gate-Source Charge	Q _{gs}	—	4.2	—		
Gate-Drain Charge	Q _{gd}	—	0.9	—		
Turn-On Delay Time	t _{D(ON)}	—	5.4	—	ns	V _{DD} = 20V, V _{GS} = 10V R _g = 3.5Ω, I _D = 25A
Turn-On Rise Time	t _r	—	2.5	—		
Turn-Off Delay Time	t _{D(OFF)}	—	16.1	—		
Turn-Off Fall Time	t _f	—	4.5	—		
Body Diode Reverse Recovery Time	t _{RR}	—	61.3	—	ns	I _F = 25A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	—	52.1	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

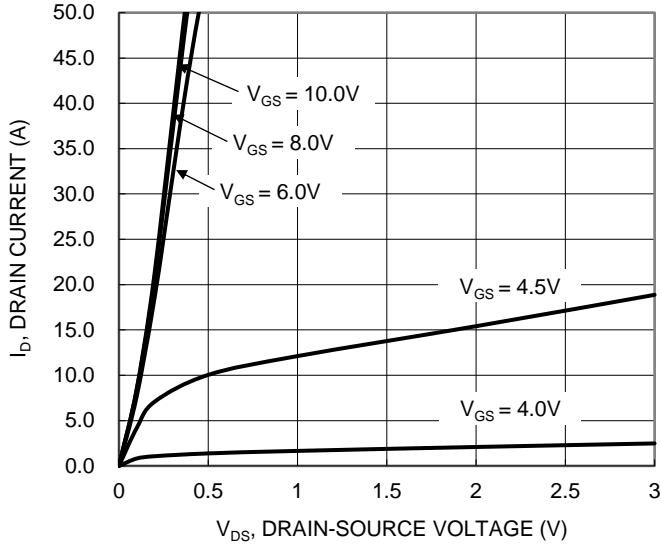


Figure 1. Typical Output Characteristic

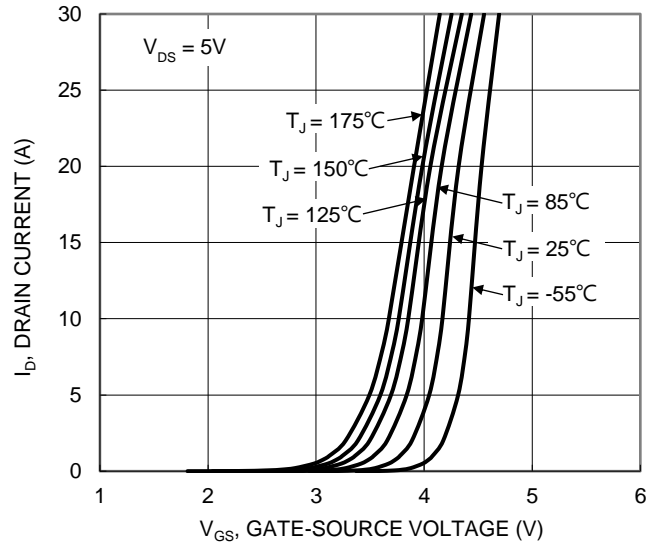


Figure 2. Typical Transfer Characteristic

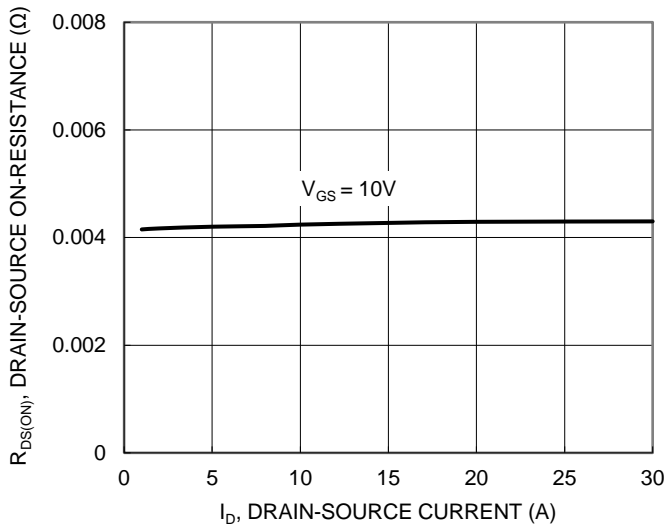


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

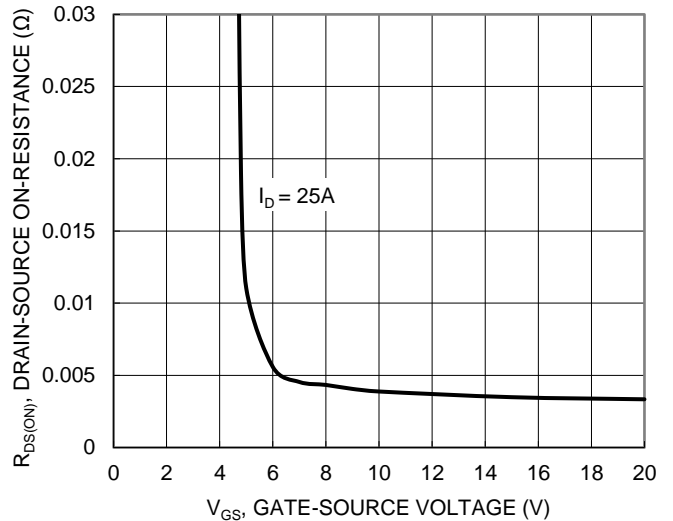


Figure 4. Typical Transfer Characteristic

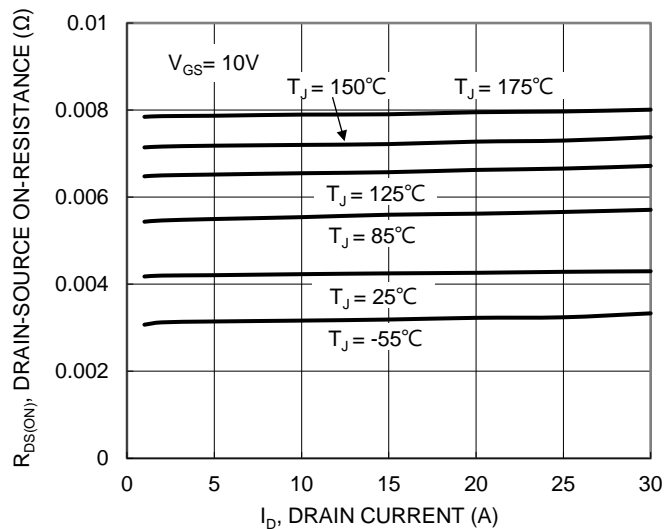


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

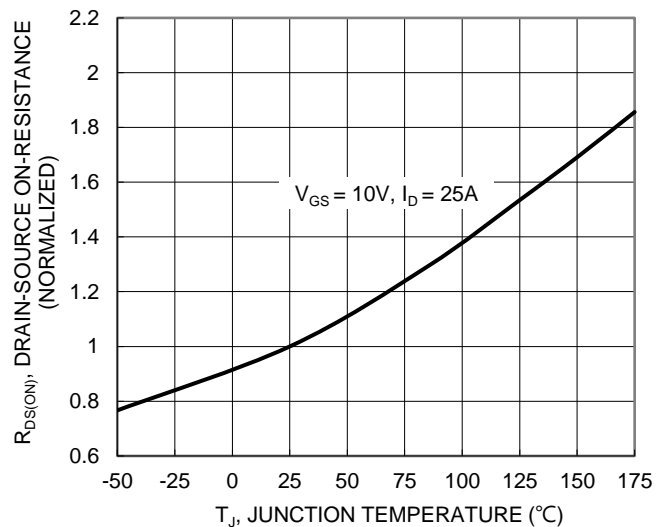


Figure 6. On-Resistance Variation with Junction Temperature

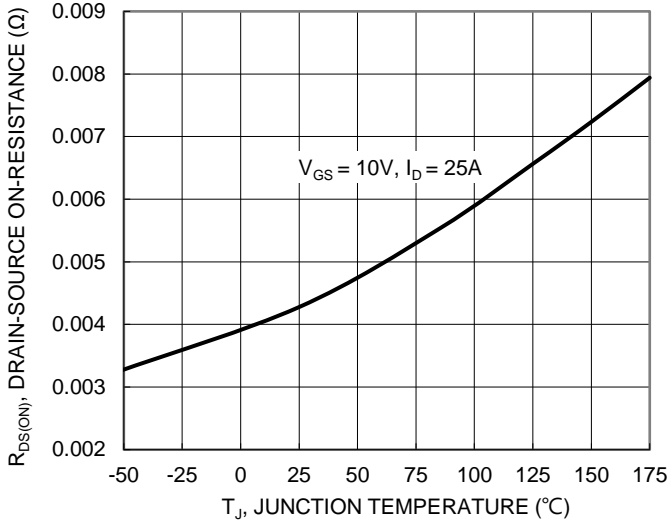


Figure 7. On-Resistance Variation with Junction Temperature

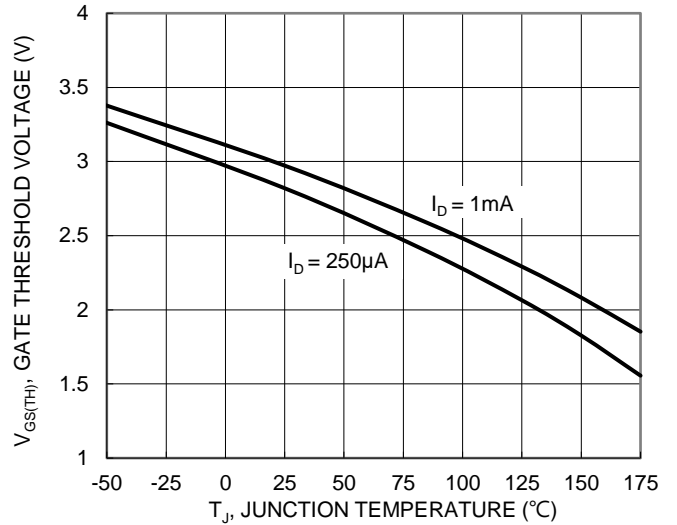


Figure 8. Gate Threshold Variation vs. Junction Temperature

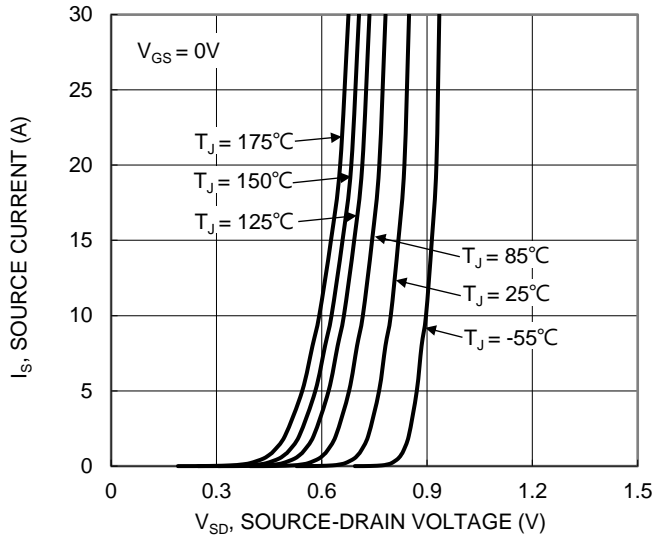


Figure 9. Diode Forward Voltage vs. Current

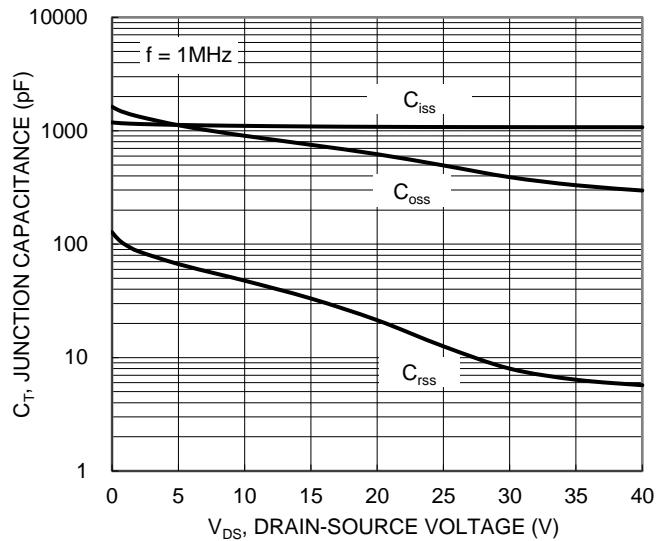


Figure 10. Typical Junction Capacitance

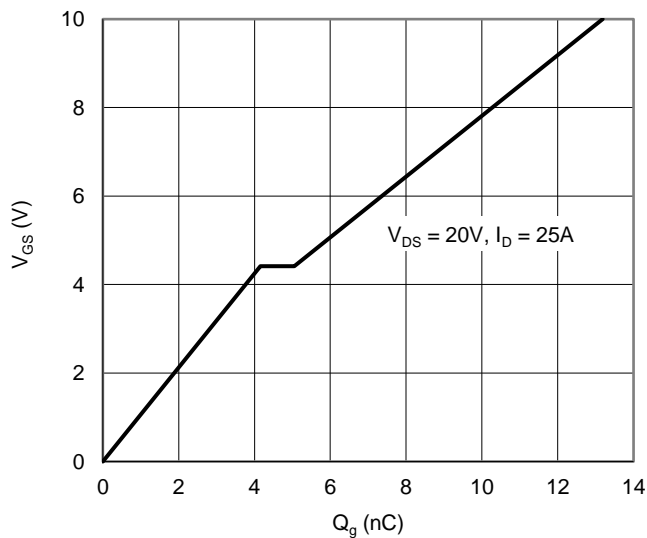


Figure 11. Gate Charge

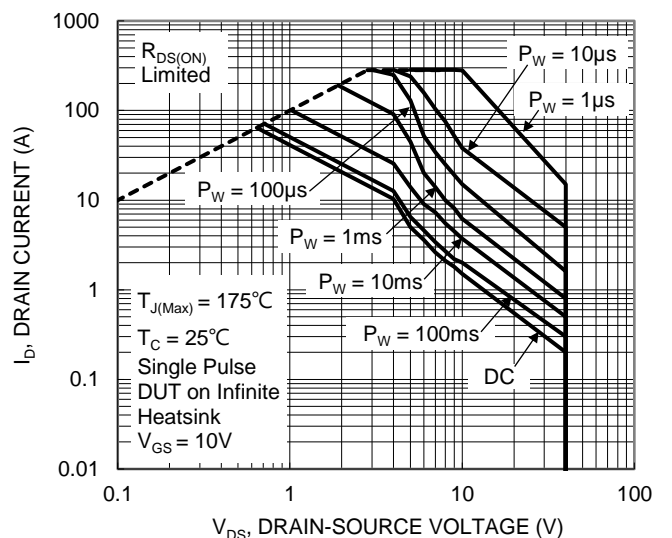


Figure 12. SOA, Safe Operation Area

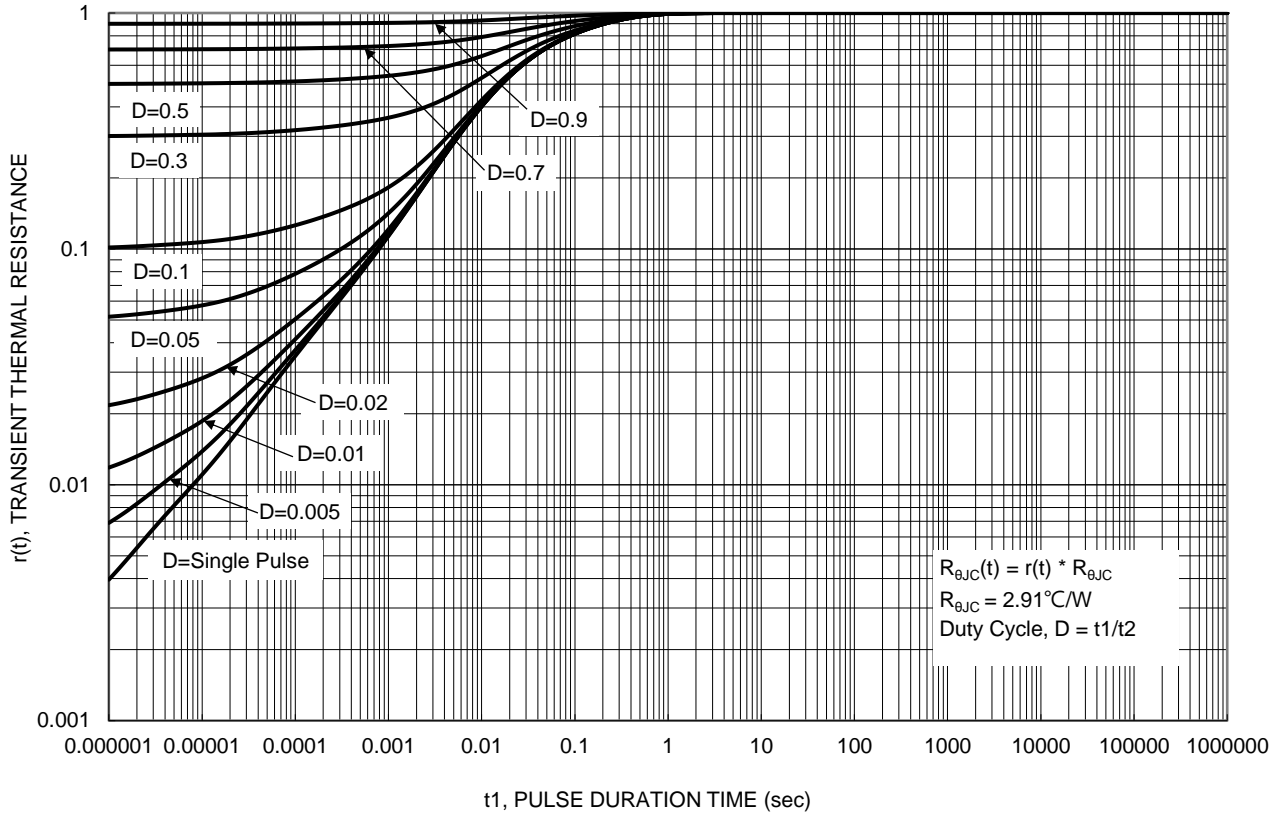
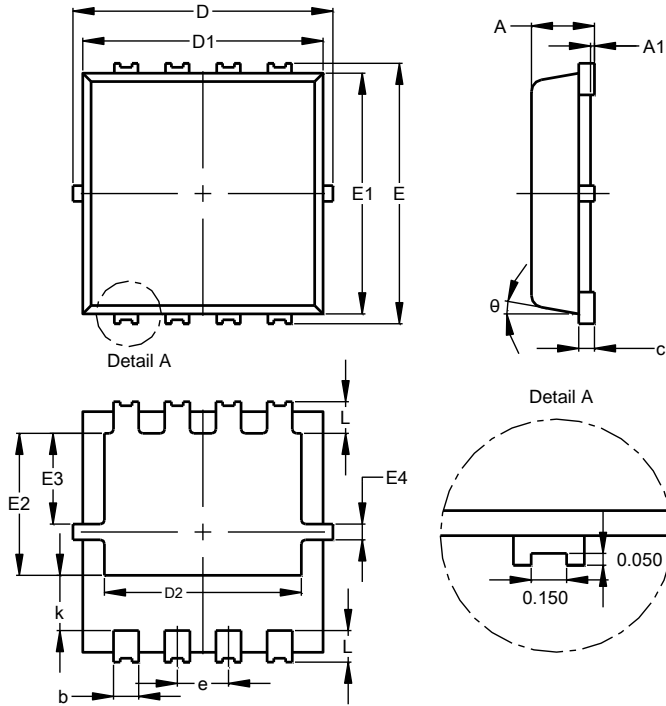


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (SWP) (Type UX)

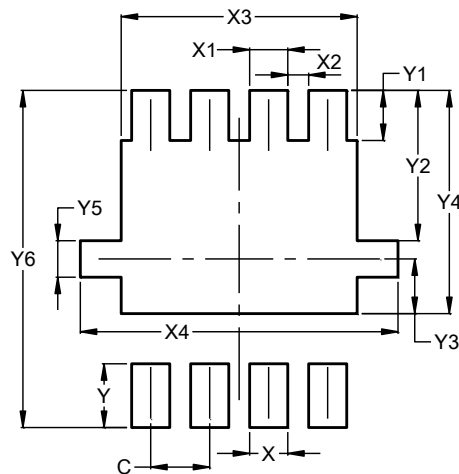


PowerDI3333-8 (SWP) (Type UX)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	2.30	2.70	2.50
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E3	0.95	1.35	1.15
E4	0.10	0.30	0.20
e	--	--	0.65
k	0.50	0.90	0.70
L	0.30	0.50	0.40
θ	0°	12°	10°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (SWP) (Type UX)



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.600
X4	3.500
Y	0.700
Y1	0.550
Y2	1.650
Y3	0.600
Y4	2.450
Y5	0.400
Y6	3.700

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