

## FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Low Switching Loss Device
- High Current Density
- Isolated AISiC Base With AlN Substrates

## APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Smart Grid

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1500ESM33-PS500 is a single switch 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## ORDERING INFORMATION

Order As:

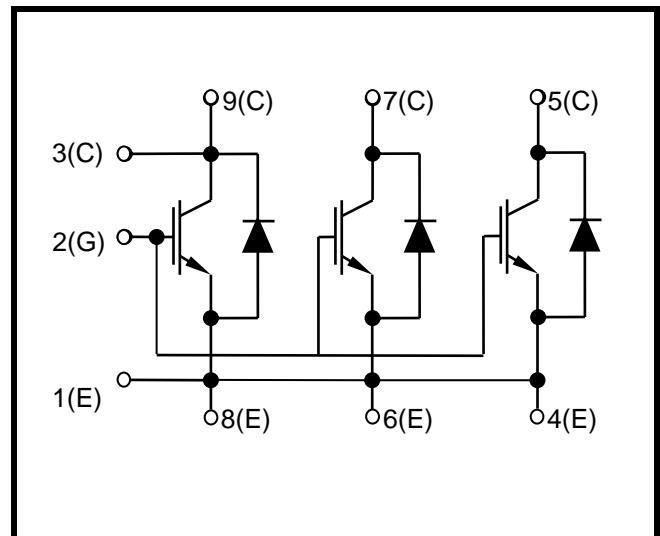
### DIM1500ESM33-PS500

Note: When ordering, please use the complete part number

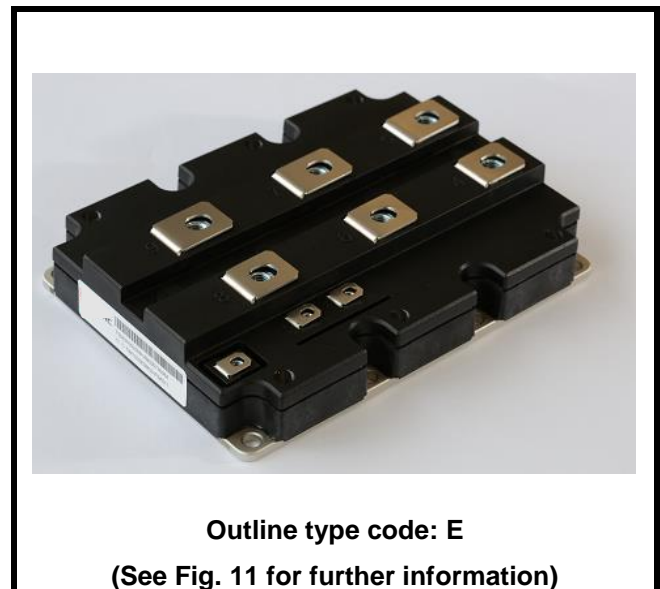
## KEY PARAMETERS

$V_{CES}$	<b>3300V</b>
$V_{CE(sat)}$ * (typ)	<b>2.4V</b>
$I_C$ (max)	<b>1500A</b>
$I_{C(PK)}$ (max)	<b>3000A</b>

\* Measured at the auxiliary terminals



**Fig. 1 Circuit configuration**



**Fig. 2 Package**

**ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under ‘Absolute Maximum Ratings’ may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	3300	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 112°C	1500	A
I <sub>C(PK)</sub>	Peak collector current	t <sub>p</sub> = 1ms	3000	A
P <sub>max</sub>	Max. transistor power dissipation	T <sub>case</sub> = 25°C, T <sub>j</sub> = 150°C	15.6	kW
I <sup>2</sup> t	Diode I <sup>2</sup> t value	V <sub>R</sub> = 0, t <sub>p</sub> = 10ms, T <sub>j</sub> = 150°C	720	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q <sub>PD</sub>	Partial discharge – per module	IEC1287, V <sub>1</sub> = 3500V, V <sub>2</sub> = 2600V, 50Hz RMS	10	pC

**THERMAL AND MECHANICAL RATINGS**

Internal insulation material: AIN  
 Baseplate material: AISiC  
 Creepage distance: 33mm  
 Clearance: 20mm  
 CTI (Comparative Tracking Index): >600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	8	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	16	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
T <sub>j</sub>	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	150	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}C$  unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>CES</sub>	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			90	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 150^{\circ}C$			150	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	$\mu A$
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_C = 120mA, V_{GE} = V_{CE}$	5.5	6.1	7.0	V
V <sub>CE(sat)</sub> †	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 1500A$		2.4	2.9	V
		$V_{GE} = 15V, I_C = 1500A, T_j = 125^{\circ}C$		2.95	3.4	V
		$V_{GE} = 15V, I_C = 1500A, T_j = 150^{\circ}C$		3.1	3.6	V
I <sub>F</sub>	Diode forward current	DC		1500		A
I <sub>FM</sub>	Diode maximum forward current	$t_p = 1ms$		3000		A
V <sub>F</sub> †	Diode forward voltage	$I_F = 1500A$		2.1	2.6	V
		$I_F = 1500A, T_j = 125^{\circ}C$		2.25	2.7	V
		$I_F = 1500A, T_j = 150^{\circ}C$		2.25	2.7	V
C <sub>ies</sub>	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 100kHz$		165		nF
Q <sub>g</sub>	Gate charge	$\pm 15V$		14.8		$\mu C$
C <sub>res</sub>	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 100kHz$		4.2		nF
L <sub>M</sub>	Module inductance			10		nH
R <sub>INT</sub>	Internal transistor resistance			110		$\mu\Omega$
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$T_j = 150^{\circ}C, V_{CC} = 2500V$ $t_p \leq 10\mu s, V_{GE} \leq 15V$ $V_{CE(max)} = V_{CES} - L^* \times dl/dt$ IEC 60747-9		5800		A

**Note:**

† Measured at the auxiliary terminals

\* L is the circuit inductance + L<sub>M</sub>

**ELECTRICAL CHARACTERISTICS**

**T<sub>case</sub> = 25°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 1800V R <sub>g(ON)</sub> = 1.0Ω R <sub>g(OFF)</sub> = 1.5Ω C <sub>GE</sub> = 330nF L <sub>S</sub> ~ 150nH		2100		ns
t <sub>f</sub>	Fall time			540		ns
E <sub>OFF</sub>	Turn-off energy loss			2400		mJ
t <sub>d(on)</sub>	Turn-on delay time			750		ns
t <sub>r</sub>	Rise time			340		ns
E <sub>ON</sub>	Turn-on energy loss			1450		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V dI <sub>F</sub> /dt = 4800A/μs		1150		μC
I <sub>rr</sub>	Diode reverse recovery current			1250		A
E <sub>rec</sub>	Diode reverse recovery energy			1550		mJ

**T<sub>case</sub> = 125°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 1800V R <sub>g(ON)</sub> = 1.0Ω R <sub>g(OFF)</sub> = 1.5Ω C <sub>GE</sub> = 330nF L <sub>S</sub> ~ 150nH		2250		ns
t <sub>f</sub>	Fall time			570		ns
E <sub>OFF</sub>	Turn-off energy loss			2950		mJ
t <sub>d(on)</sub>	Turn-on delay time			730		ns
t <sub>r</sub>	Rise time			350		ns
E <sub>ON</sub>	Turn-on energy loss			1900		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V dI <sub>F</sub> /dt = 4800A/μs		1800		μC
I <sub>rr</sub>	Diode reverse recovery current			1420		A
E <sub>rec</sub>	Diode reverse recovery energy			2450		mJ

**T<sub>case</sub> = 150°C unless stated otherwise**

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 1500A V <sub>GE</sub> = ±15V V <sub>CE</sub> = 1800V R <sub>g(ON)</sub> = 1.0Ω R <sub>g(OFF)</sub> = 1.5Ω C <sub>GE</sub> = 330nF L <sub>S</sub> ~ 150nH		2290		ns
t <sub>f</sub>	Fall time			580		ns
E <sub>OFF</sub>	Turn-off energy loss			3200		mJ
t <sub>d(on)</sub>	Turn-on delay time			730		ns
t <sub>r</sub>	Rise time			360		ns
E <sub>ON</sub>	Turn-on energy loss			2100		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1500A V <sub>CE</sub> = 1800V dI <sub>F</sub> /dt = 5000A/μs		1980		μC
I <sub>rr</sub>	Diode reverse recovery current			1450		A
E <sub>rec</sub>	Diode reverse recovery energy			2720		mJ

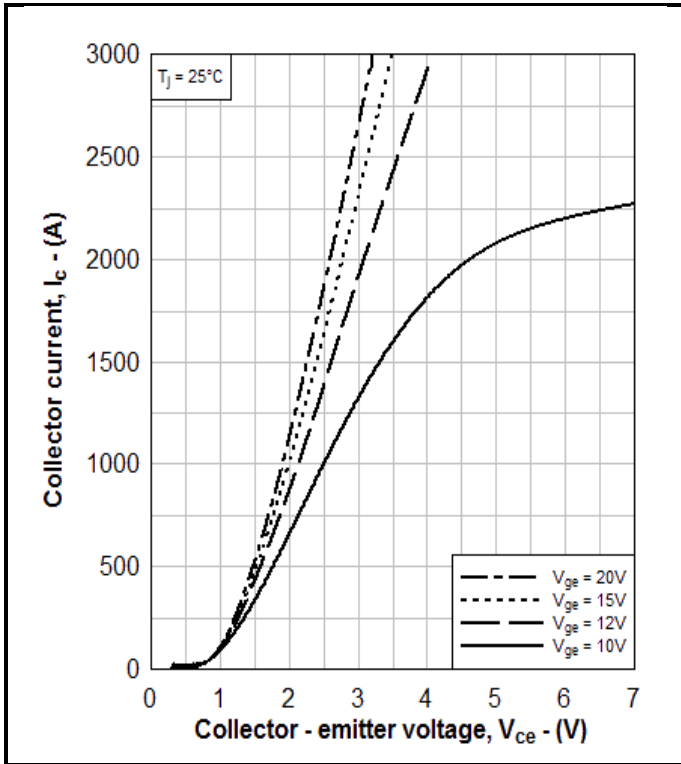


Fig. 3 Typical output characteristics

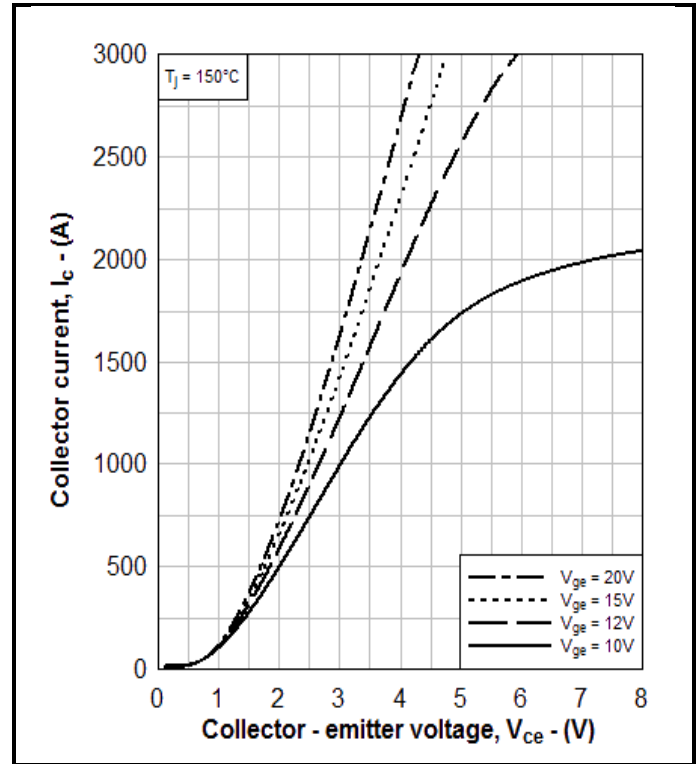


Fig. 4 Typical output characteristics

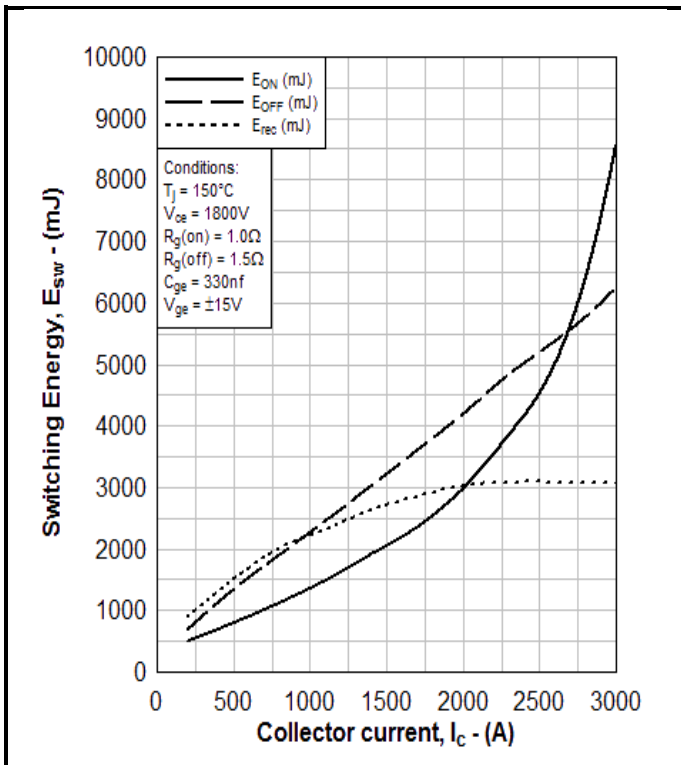


Fig. 5 Typical switching energy vs collector current

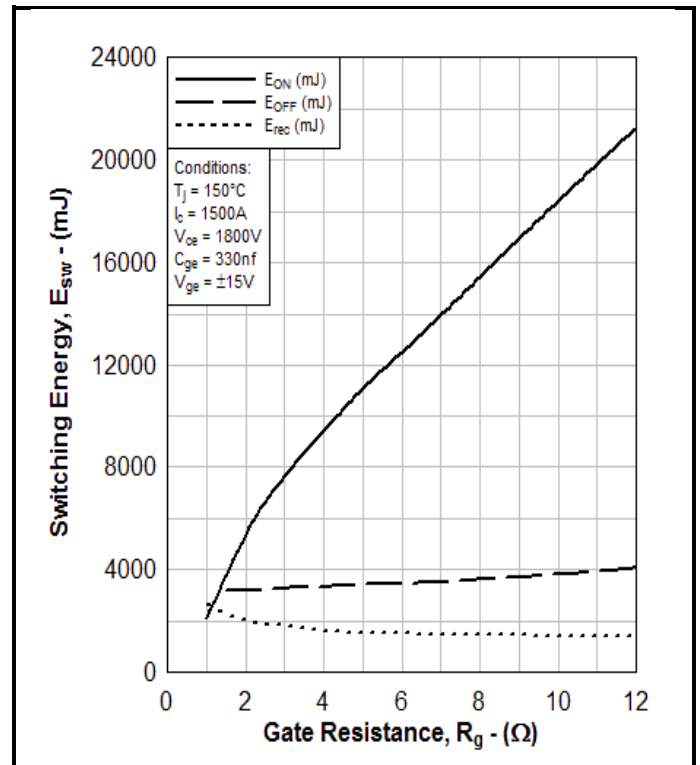


Fig. 6 Typical switching energy vs gate resistance

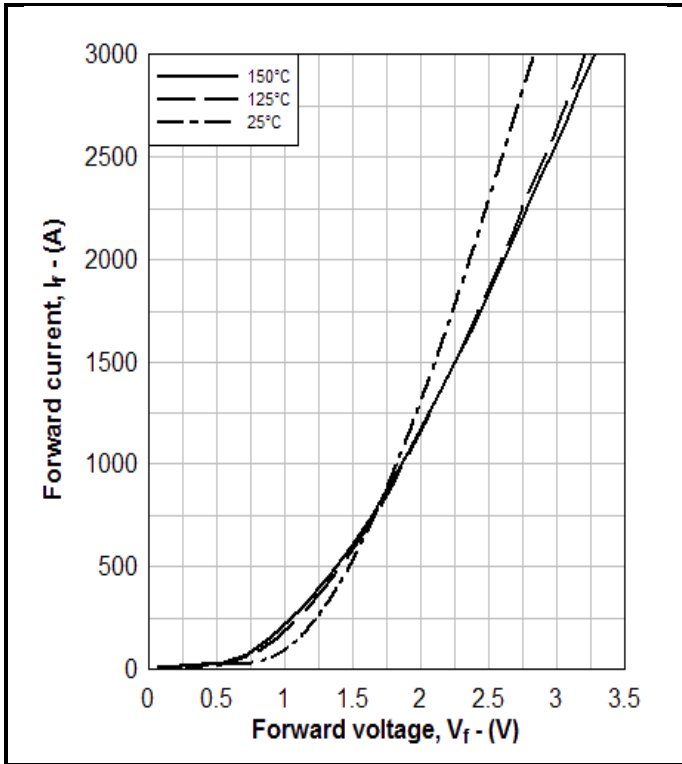


Fig. 7 Diode typical forward characteristics

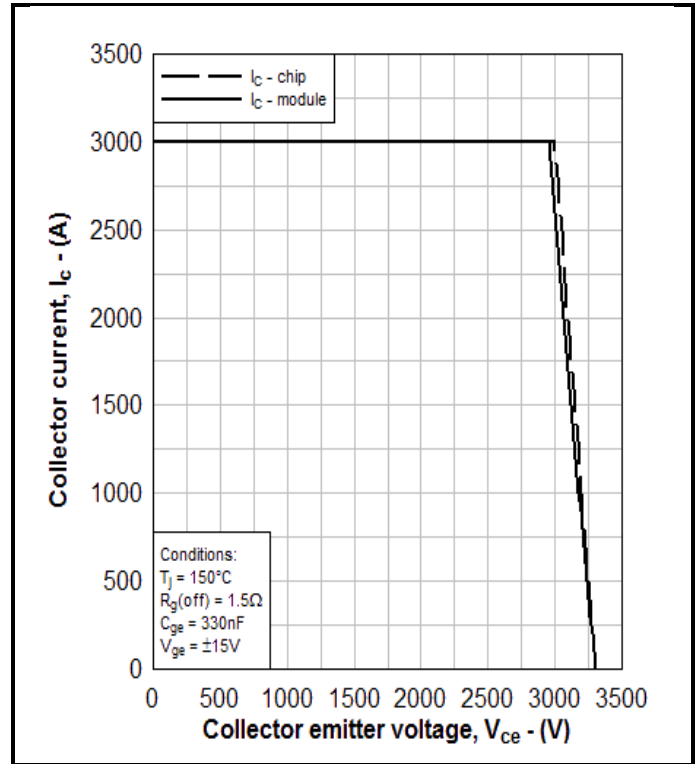


Fig. 8 Reverse bias safe operating area

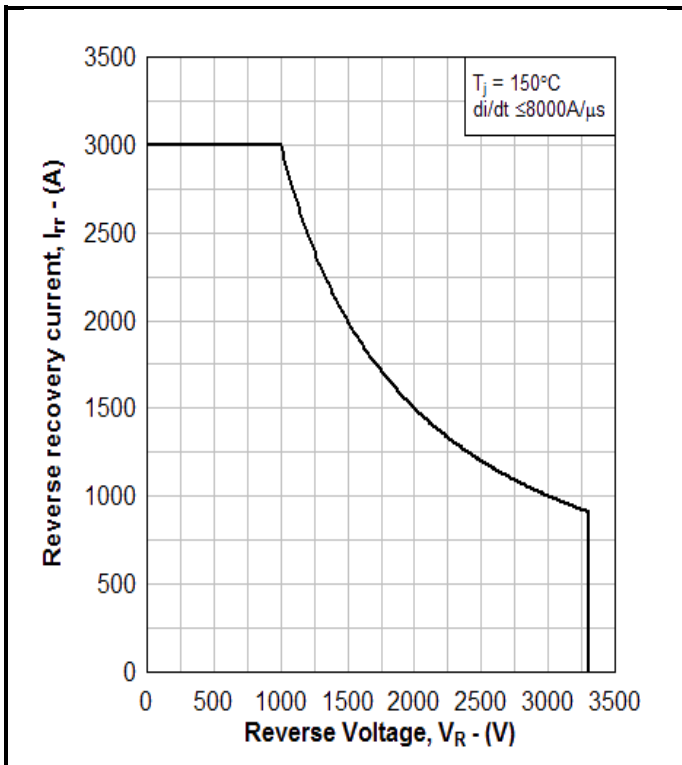


Fig. 9 Diode reverse bias safe operating area

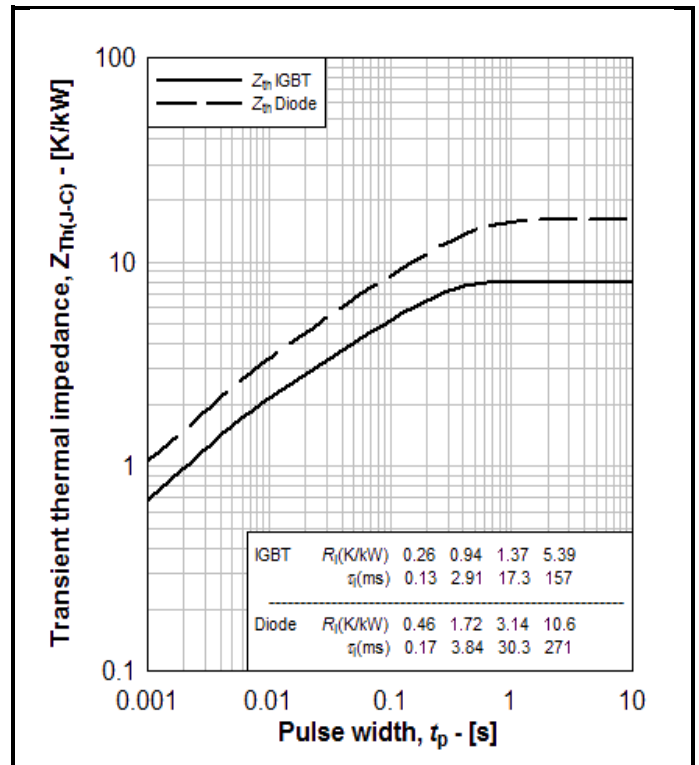
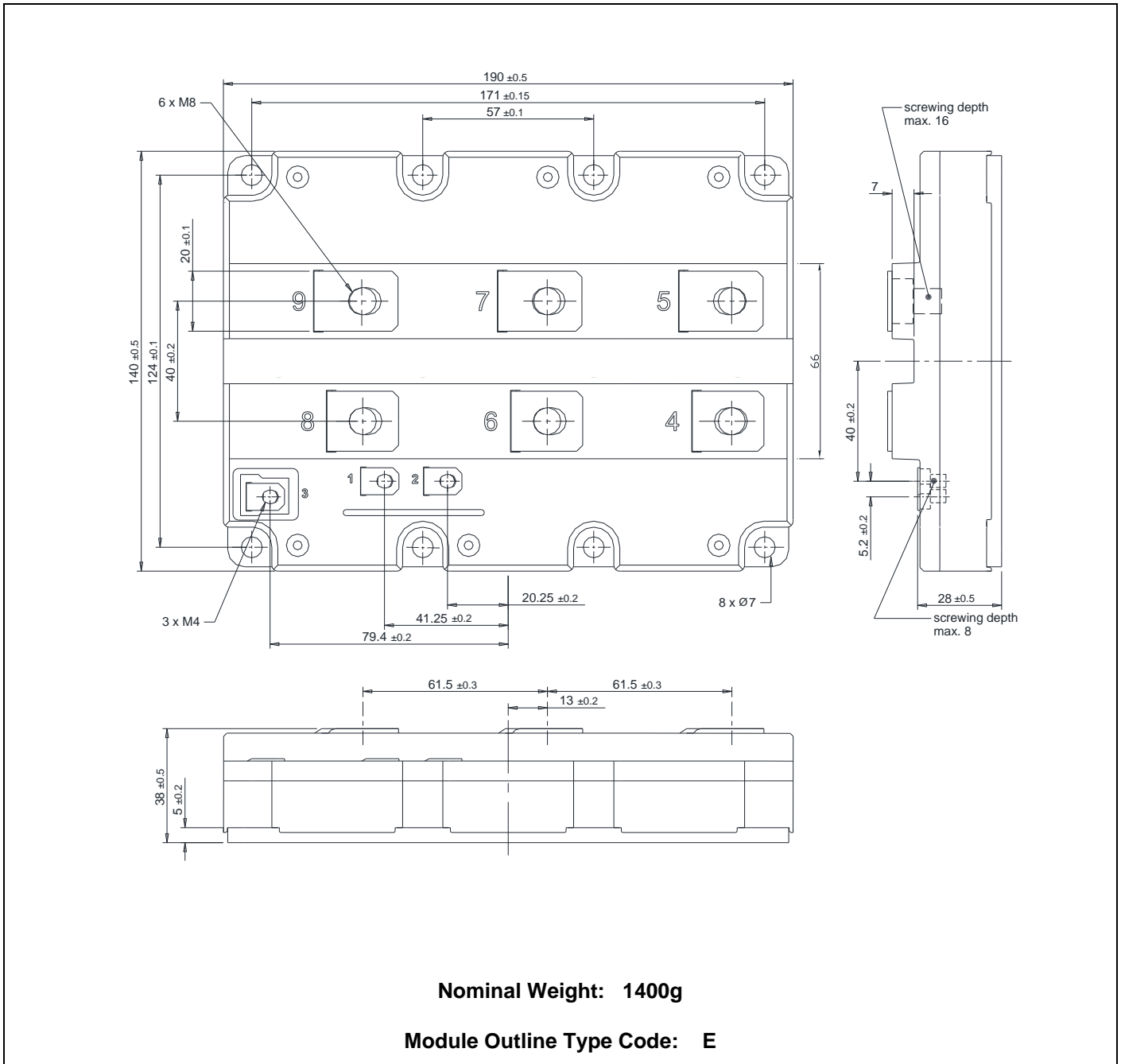


Fig. 10 Transient thermal impedance

**PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services.  
 All dimensions in mm, unless stated otherwise.  
**DO NOT SCALE.**



**Fig. 11 Module outline drawing**

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