

FEATURES

- Trench Gate IGBT
- High Thermal Cycling
- Cu Base with Enhanced Al₂O₃ Substrates
- 10µs Short Circuit Withstand

APPLICATIONS

- Wind Turbines
- Power Charging Equipment
- Smart Grid
- High Reliability Inverters

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM600M1HS17-PA500 is a half bridge 1700V, trench gate, insulated gate bipolar transistor (IGBT) module with enhanced field stop and implantation technology. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM600M1HS17-PA500

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}	1700V
$V_{CE(sat)}$ * (typ)	1.80V
I_C (max)	600A
$I_{C(RM)}$ (max)	1200A

* Measured at the auxiliary terminals

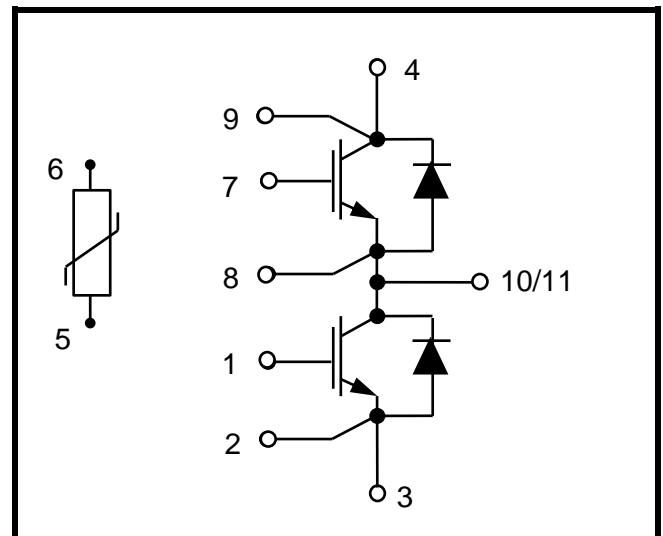


Fig. 1 Circuit configuration



Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under ‘Absolute Maximum Ratings’ may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V, T _C = 25°C	1700	V
V _{GES}	Gate-emitter voltage	T _C = 25°C	±20	V
I _C	Continuous collector current	T _C = 100°C, T _{vjmax} = 175°C	600	A
I _{C(PK)}	Peak collector current	t _p = 1ms	1200	A
P _{max}	Max. transistor power dissipation	T _C = 25°C, T _{vj} = 175°C	3.26	kW
I ² t	Diode I ² t value	V _R = 0, t _p = 10ms, T _{vj} = 150°C	41.5	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	3400	V

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	Al ₂ O ₃
Baseplate material:	Cu
Creepage distance – Terminal to heatsink:	14.5mm
Creepage distance – Terminal to terminal:	13.0mm
Clearance – Terminal to heatsink:	12.5mm
Clearance – Terminal to terminal:	10mm
CTI (Comparative Tracking Index):	>200

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R _{th(j-c)}	Thermal resistance – IGBT	Continuous dissipation – junction to case			46	°C/kW
R _{th(j-c)}	Thermal resistance – diode				80	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (IGBT)	Mounting torque 5Nm (with mounting grease 1W/m °C)		33		°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (Diode)			38		°C/kW
T _j	Junction temperature	IGBT	-40		150	°C
		Diode	-40		150	°C
T _{stg}	Storage temperature range	-	-40		125	°C
	Screw torque	Mounting – M5	3		6	Nm
		Electrical connections – M6	3		6	Nm

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}C$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I _{CES}	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES}			1	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _C = 125°C			15	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _C = 150°C			30	mA
I _{GES}	Gate leakage current	V _{GE} = ± 20V, V _{CE} = 0V			0.5	μA
V _{GE(TH)}	Gate threshold voltage	I _C = 15mA, V _{GE} = V _{CE}	5.60	6.20	6.80	V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 600A		1.80	2.20	V
		V _{GE} = 15V, I _C = 600A, T _j = 125°C		2.20		V
		V _{GE} = 15V, I _C = 600A, T _j = 150°C		2.30		V
I _F	Diode forward current	DC		600		A
I _{FM}	Diode maximum forward current	t _p = 1ms		1200		A
V _F	Diode forward voltage	I _F = 600A		1.85	2.25	V
		I _F = 600A, T _j = 125°C		2.10		V
		I _F = 600A, T _j = 150°C		2.10		V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 100kHz		96		nF
Q _g	Gate charge	±15V		6.1		μC
C _{res}	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 100kHz		0.7		nF
L _M	Module inductance			20		nH
R _{INT}	Internal transistor resistance			1		mΩ
SC _{Data}	Short circuit current, I _{sc}	T _j = 150°C, V _{CC} = 1000V t _p ≤ 10μs, V _{GE} ≤ 15V V _{CE(max)} = V _{CES} - L* x di/dt IEC 60747-9		2400		A

Note:

* L is the circuit inductance + L_M

NTC-Thermistor Data

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
R ₂₅	Rated resistance	T _C = 25°C		5		kΩ
ΔR/R	Deviation of R ₁₀₀	T _C = 100°C, R ₁₀₀ = 493Ω	-5		5	%
P ₂₅	Power dissipation	T _C = 25°C			20	mW
B _{25/50}	B-value	R ₂ = R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298.15K))]		3375		K
B _{25/80}		R ₂ = R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298.15K))]		3411		K
B _{25/100}		R ₂ = R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298.15K))]		3433		K

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 600A V _{CE} = 900V V _{GE} = ±15V R _{G(OFF)} = 1.0Ω R _{G(ON)} = 1.0Ω L _S ~ 60nH	dv/dt = 5000V/μs		760		ns
t _f	Fall time				360		ns
E _{OFF}	Turn-off energy loss				135		mJ
t _{d(on)}	Turn-on delay time		di/dt = 7700A/μs		285		ns
t _r	Rise time				81		ns
E _{ON}	Turn-on energy loss				45		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 600A V _{CE} = 900V di/dt = 7700A/μs			155		μC
I _{rr}	Diode reverse recovery current				720		A
E _{rec}	Diode reverse recovery energy				123		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 600A V _{CE} = 900V V _{GE} = ±15V R _{G(OFF)} = 1.0Ω R _{G(ON)} = 1.0Ω L _S ~ 60nH	dv/dt = 5000V/μs		860		ns
t _f	Fall time				500		ns
E _{OFF}	Turn-off energy loss				183		mJ
t _{d(on)}	Turn-on delay time		di/dt = 7700A/μs		295		ns
t _r	Rise time				85		ns
E _{ON}	Turn-on energy loss				61		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 600A V _{CE} = 900V di/dt = 7700A/μs			250		μC
I _{rr}	Diode reverse recovery current				810		A
E _{rec}	Diode reverse recovery energy				192		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 600A V _{CE} = 900V V _{GE} = ±15V R _{G(OFF)} = 1.0Ω R _{G(ON)} = 1.0Ω L _S ~ 60nH	dv/dt = 5000V/μs		870		ns
t _f	Fall time				535		ns
E _{OFF}	Turn-off energy loss				194		mJ
t _{d(on)}	Turn-on delay time		di/dt = 7700A/μs		300		ns
t _r	Rise time				86		ns
E _{ON}	Turn-on energy loss				67		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 600A V _{CE} = 900V di/dt = 7700A/μs			270		μC
I _{rr}	Diode reverse recovery current				830		A
E _{rec}	Diode reverse recovery energy				209		mJ

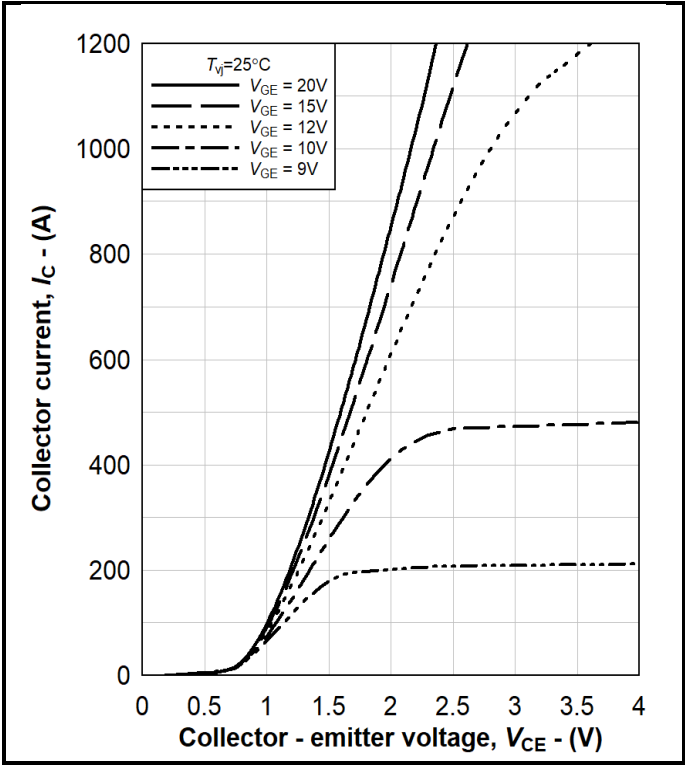


Fig. 3 Typical IGBT output characteristics, $I_C = f(V_{CE})$

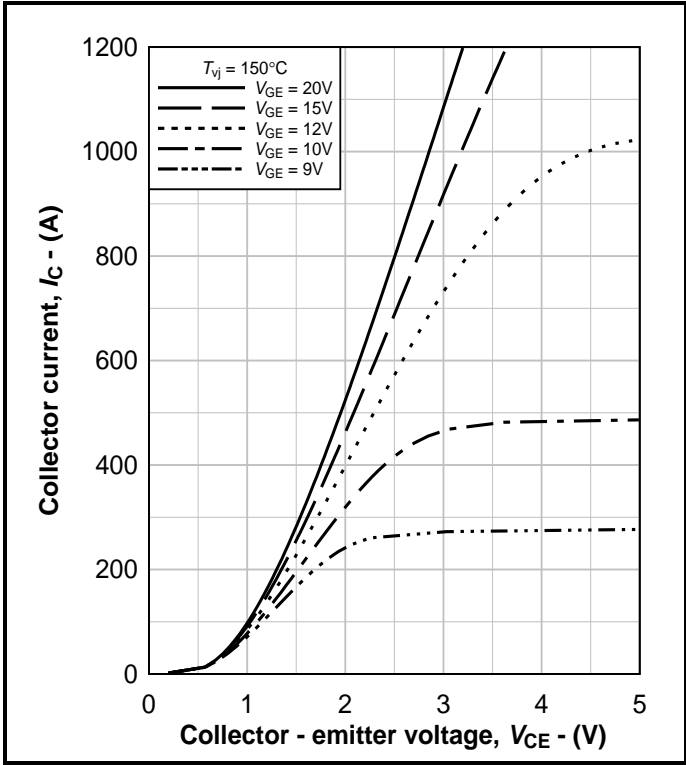


Fig. 4 Typical IGBT output characteristics, $I_C = f(V_{CE})$

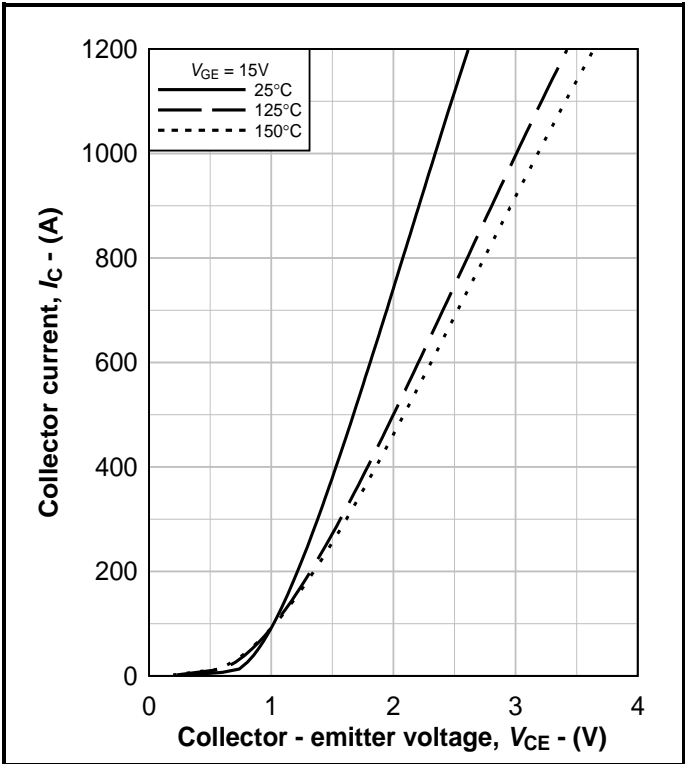


Fig. 5 Typical IGBT output characteristics, $I_C = f(V_{CE})$

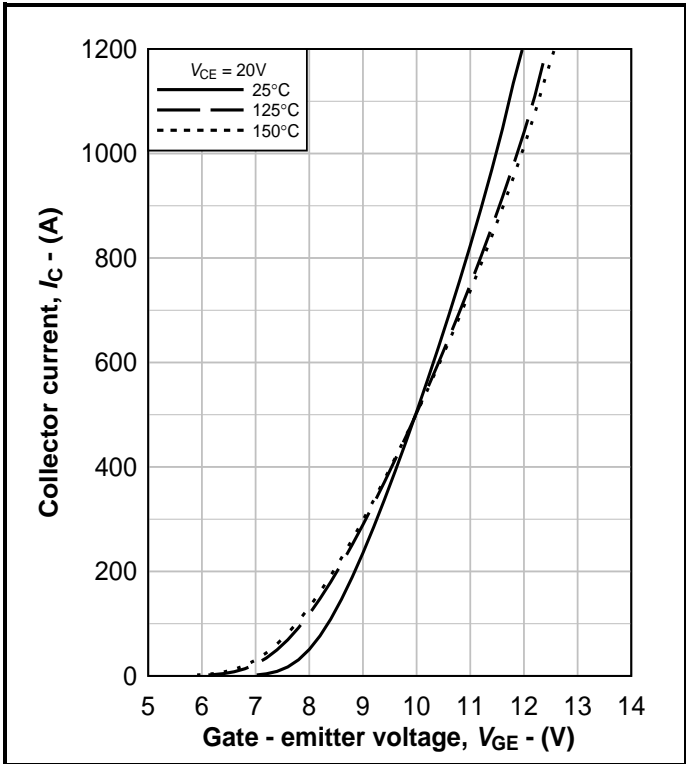


Fig. 6 Typical IGBT transfer characteristics, $I_C = f(V_{GE})$

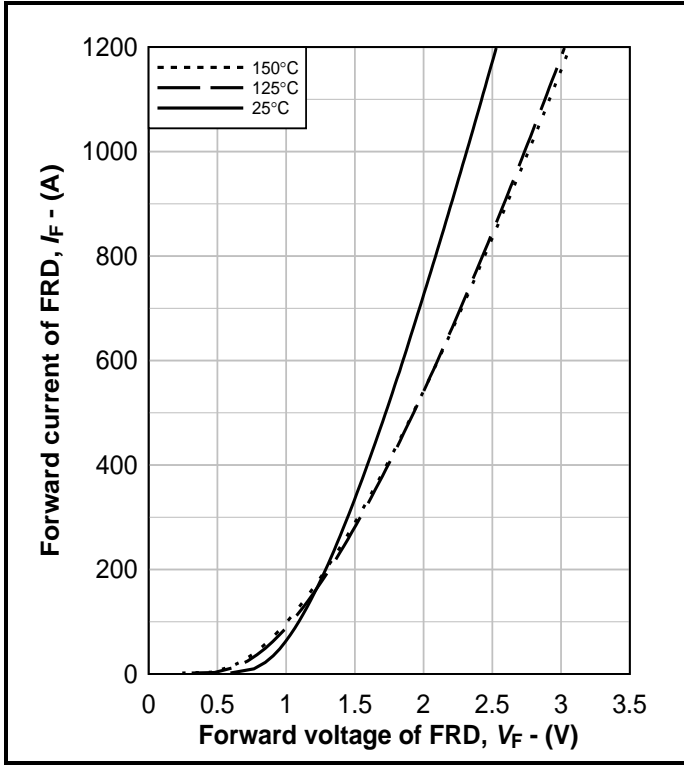


Fig. 7 Typical FRD E_{rec} , $E_{rec} = f(R_G)$

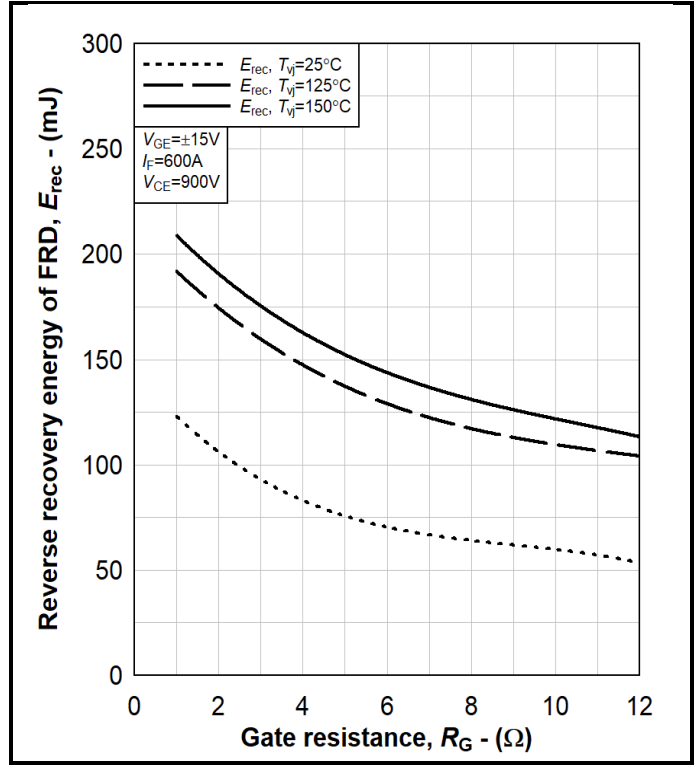


Fig. 8 Typical FRD E_{rec} , $E_{rec} = f(R_G)$

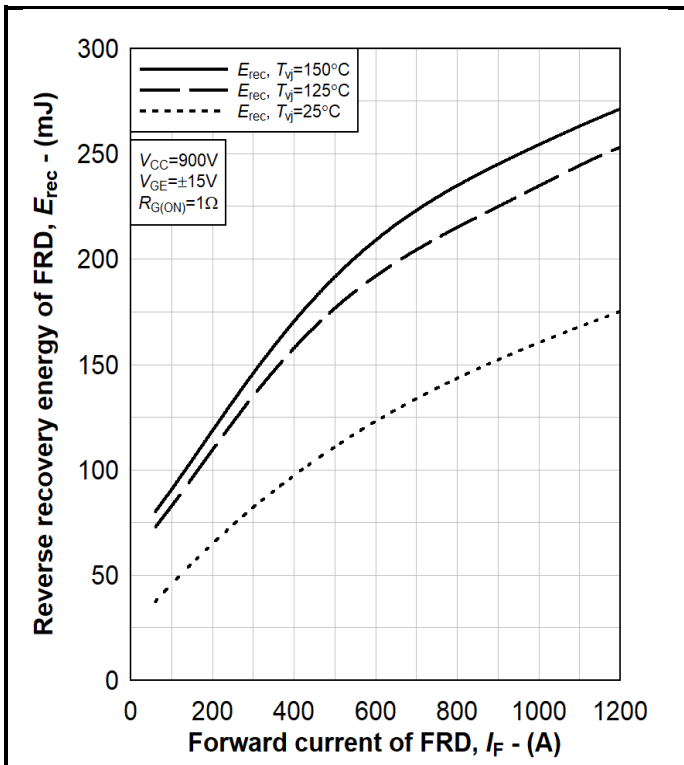


Fig. 9 Typical FRD E_{rec} , $E_{rec} = f(I_F)$

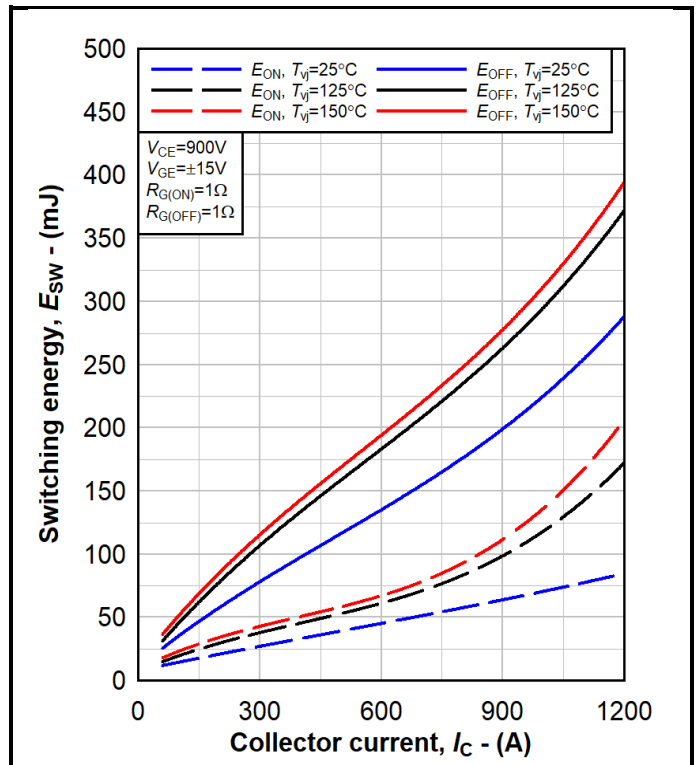


Fig. 10 Typical IGBT switching energy, $E_{ON} = f(I_C)$, $E_{OFF} = f(I_C)$

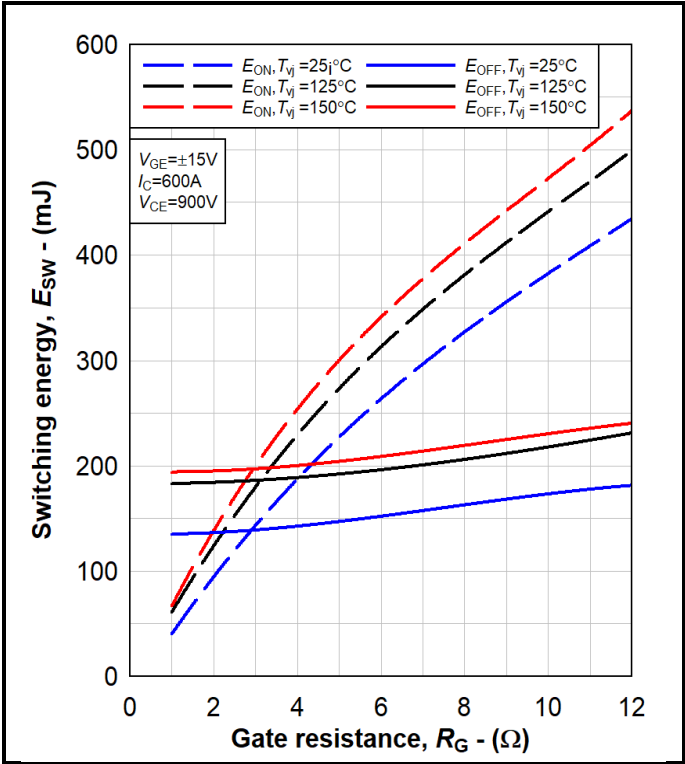


Fig. 11 Typical IGBT switching energy $E_{ON} = f(R_G)$, $E_{OFF} = f(R_G)$

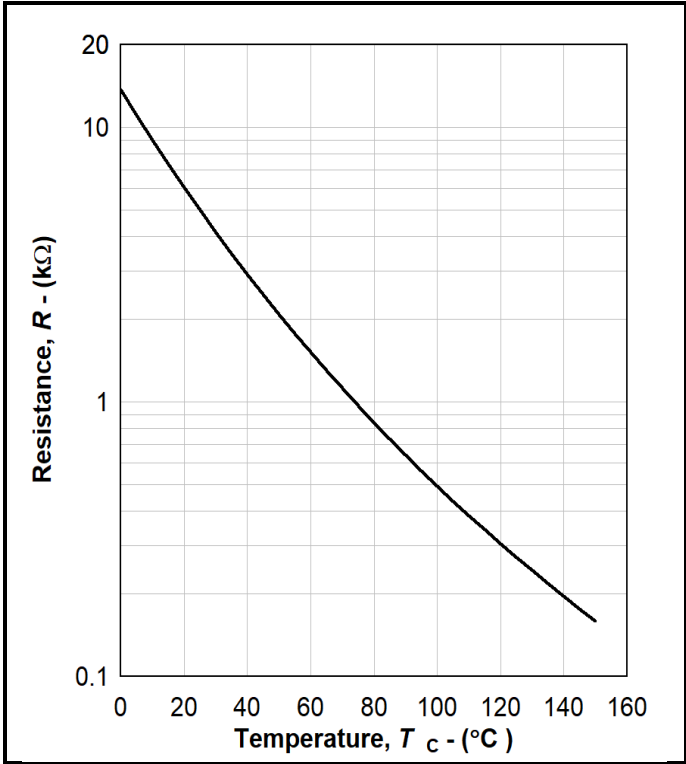


Fig. 12 Typical NTC thermistor characteristic, $R = f(T_c)$

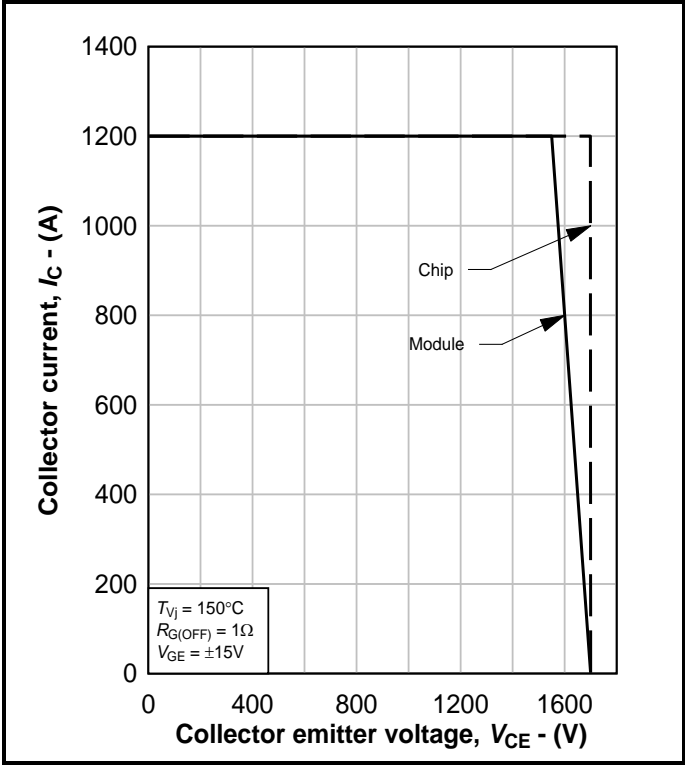


Fig. 13 Reverse bias safe operating area of IGBT, $I_c = f(V_{CE})$

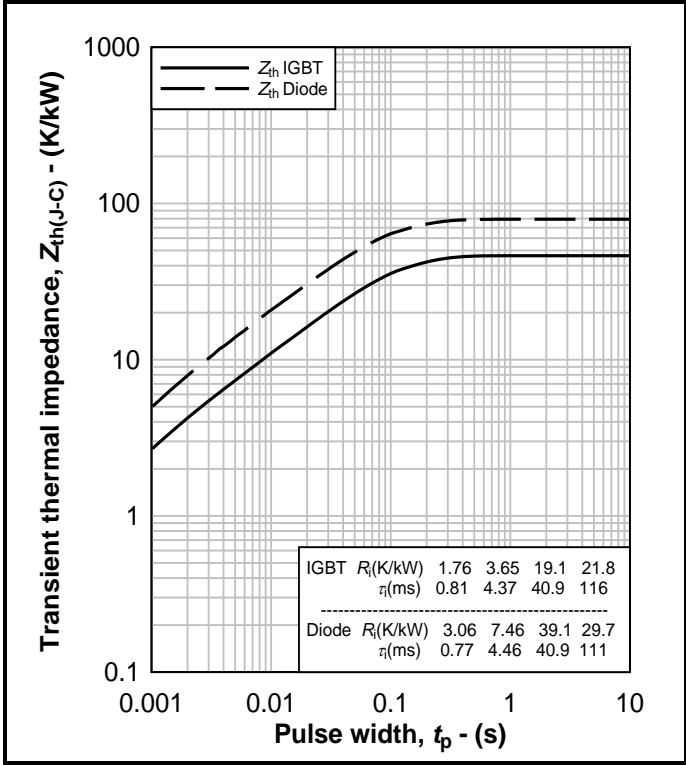


Fig. 14 Transient thermal impedance, $Z_{th(J-C)} = f(t)$

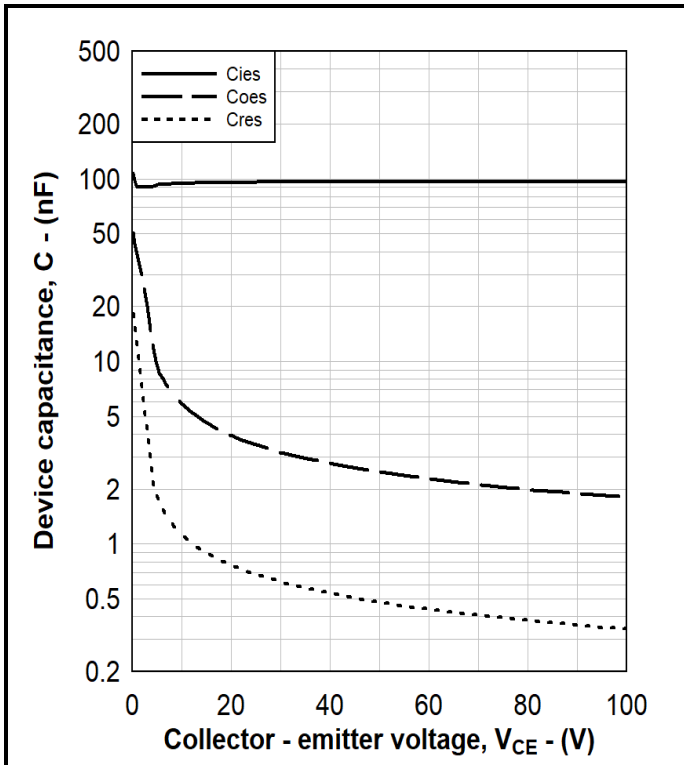


Fig. 15 Device capacitance

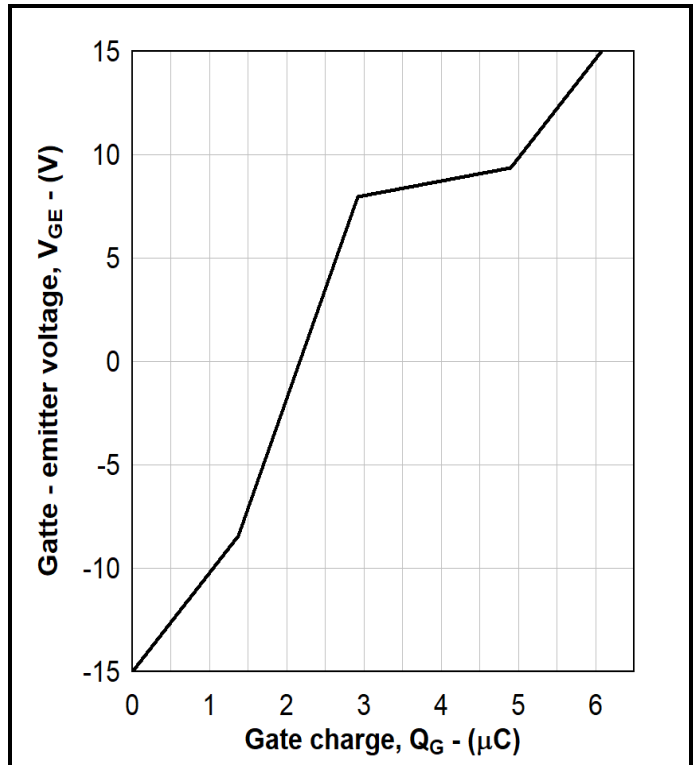


Fig. 16 Typical gate charge characteristics

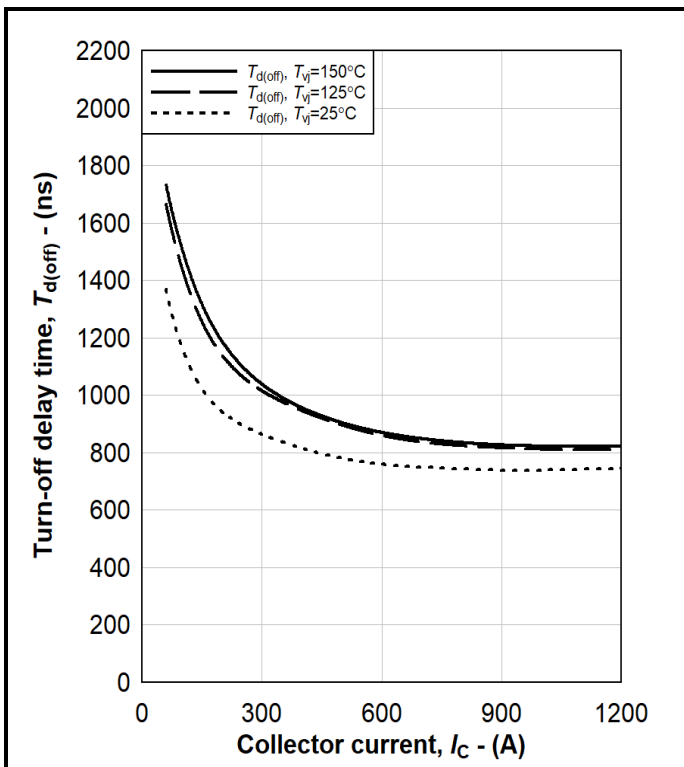


Fig. 17 Turn off delay time vs collector current

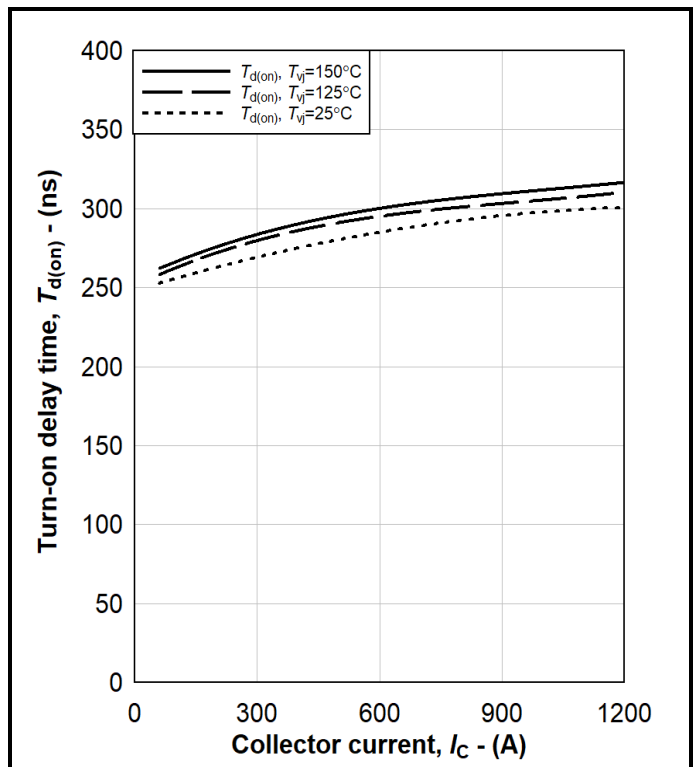


Fig. 18 Turn on delay time vs collector current

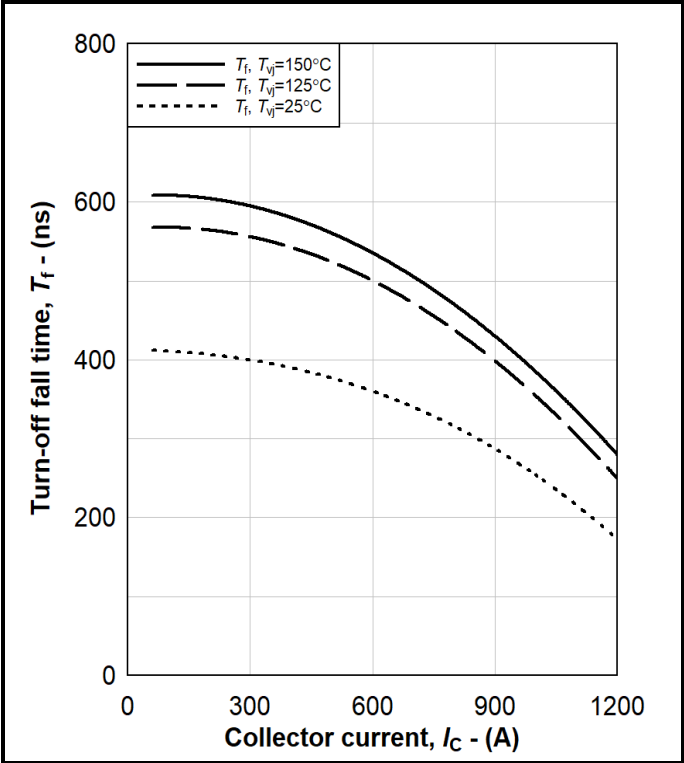


Fig. 19 Turn-off time vs collector current

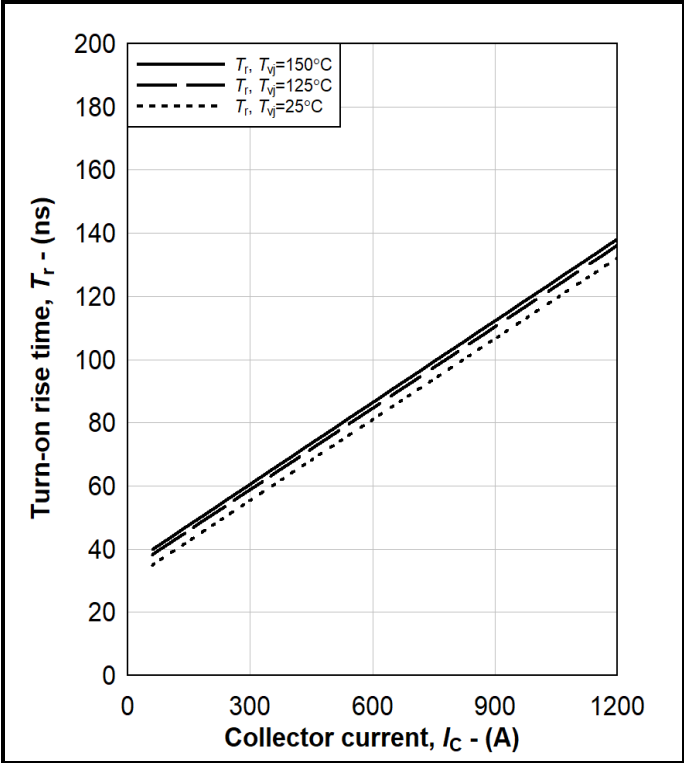


Fig. 20 Turn-on rise time vs collector current

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.
 All dimensions in mm, unless stated otherwise.
DO NOT SCALE.

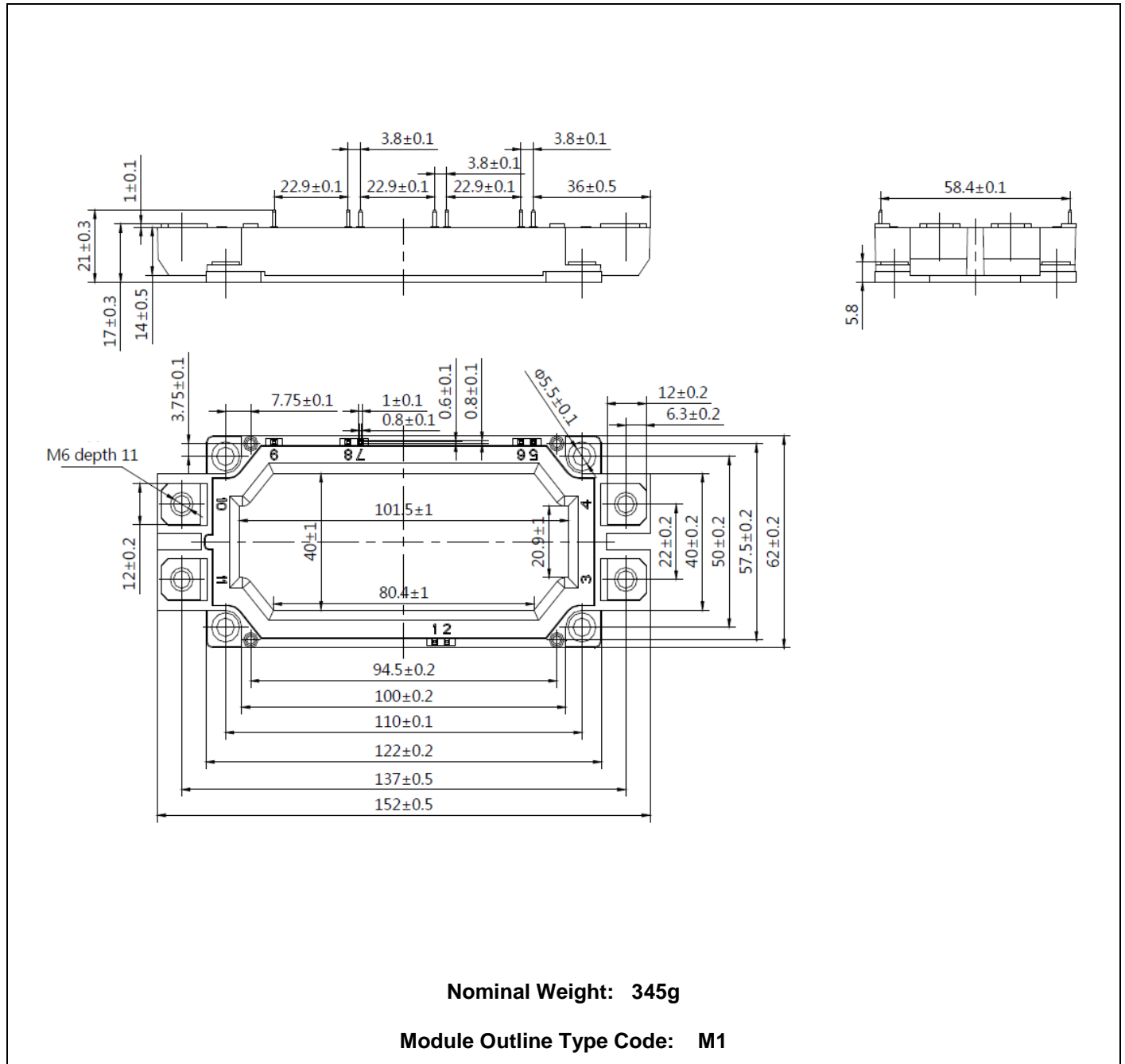


Fig. 15 Module outline drawing

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