

HIGH RELIABILITY HYBRID DC-DC CONVERTERS

DESCRIPTION

The DVTR series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVTR series is a magnetic feedback circuit that is radiation immune. Operating at a nominal fixed frequency of 500 kHz, these regulated, isolated units utilize well controlled undervoltage lockout circuitry to eliminate slow start-up problems.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6.118,673

FEATURES

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 40 Watts Output Power
- Radiation Immune Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Radiation Hardened Version Available
- Precision Seam Welded or Solder Seal Hermetic Package
- High Power Density: > 40 W/in³
- Custom Versions Available
- Additional Environmental Screening Available
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With a DVMC28 EMI Filter
- Flanged and Non-flanged Versions Available.
- MIL-PRF-38534 Element Evaluated Components

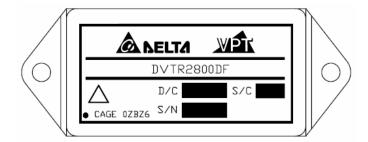


Figure 1 – DVTR2800D / DVTR2800DF DC-DC Converter (Not To Scale)



SPECIFICATIONS ($T_{CASE} = -55^{\circ}C$ to $+125^{\circ}C$, $V_{IN} = +28V \pm 5\%$, Full Load⁵, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous) $50 V_{DC}$ Input Voltage (Transient, 1 second) 80 Volts

Output Power¹ Lead Solder Temperature (10 seconds) 40 Watts

Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}C$) 13 Watts

Junction Temperature Rise to Case +15°C Storage Temperature -65°C to +150°C

270°C

Weight (Maximum) (Un-Flanged / Flanged) (49 / 52) Grams

Parameter		Conditions		OVTR2805)	ı	DVTR2812I)	Units
Farameter		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT		Continuous	15	28	50	15	28	50	V
Voltage ⁴		Transient, 1 sec	-	-	80	-	-	80	V
Current		Inhibited	-	-	6	-	-	6	mA
Current		No Load	-	-	90	-	-	90	mA
Ripple Current		Full Load ⁵ , 20Hz to 10MHz	-	-	50	-	-	50	mA_{p-p}
Inhibit Pin Input⁴			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit \	/oltage⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	11.0	-	14.5	V
	+V _{OUT}	T _{CASE} = 25°C	4.95	5.0	5.05	11.88	12.0	12.12	V
OUTPUT	$+V_{OUT}$	T _{CASE} = -55°C to +125°C	4.925	5.0	5.075	11.82	12.0	12.18	V
Voltage ⁵	$-V_{OUT}$	T _{CASE} = 25°C	4.80	5.0	5.20	11.80	12.0	12.20	V
	$-V_{OUT}$	T _{CASE} = -55°C to +125°C	4.70	5.0	5.30	11.64	12.0	12.36	V
Power ^{3,6}	Total		0	-	30	0	-	40	W
Power ^{3,3}	$\pm V_{\text{OUT}}$	Either Output	0	-	21	0	-	28	W
Current ^{3,6}	±V _{OUT}	Either Output	0	-	4.2	0	-	2.33	Α
Ripple Voltage	$\pm V_{OUT}$	Full Load ⁵ , 20Hz to 10MHz	-	-	60	-	-	50	mV_{p-p}
Line Degulation	+V _{OUT}	V _{IN} = 16V to 40V	-	-	20	-	-	20	mV
Line Regulation	$-V_{\text{OUT}}$	V _{IN} = 16V to 40V	-	-	200	-	-	200	mV
Load Deculation	+V _{OUT}	No Load to Full Load ⁵	-	-	50	-	-	50	mV
Load Regulation	$-V_{OUT}$	No Load to Full Load ^{5,7}	-	-	200	-	-	200	mV
Cross Regulation	-V _{OUT}	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	650	-	-	650	mV
EFFICIENCY		Full Load ⁵	73	-	-	78	-	-	%
LOAD FALL T DOMED DIO	UDATION	Overload ⁴	-	-	16	-	-	14	W
LOAD FAULT POWER DISS	SIPATION	Short Circuit	-	-	16	-	-	14	W
CAPACITIVE LOAD ⁴		Either Output	-	-	500	-	-	500	μF
SWITCHING FREQUENCY			400	450	550	400	450	550	kHz
SYNC FREQUENCY RANG	E	V _H - V _L = 5V, DC = 20-80%	500	-	600	500	-	600	kHz
ISOLATION		500 V _{DC} , T _{CASE} = 25°C	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T _C = 55°C	-	413	-	-	413	-	kHrs

See notes next page.



SPECIFICATIONS (T_{CASE} = -55°C to +125°C, V_{IN} = +28V ± 5%, Full Load⁵, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS								
Input Voltage (Continuous)	50 V _{DC}	Junction Temperature Rise to Case	+15°C					
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C					
Output Power ¹	40 Watts	Lead Solder Temperature (10 seconds)	270°C					
Power Dissipation (Full Load, T _{CASE} = +125°C)	13 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(49 / 52) Grams					

Parameter		Conditions	DVTR2805D			DVTR2812D			Units
		Conditions	Min	Тур	Max	Min	Тур	Max	Offics
DYNAMIC									
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	400	-	-	450	mV_{PK}
Load Step Recovery ²		Tiali Load to Full Load	-	-	350	-	-	400	μSec
Line Step Output Transient ⁴	±V _{OUT}	V _{IN} = 16V to 40V	-	300	600	-	500	900	mV_{PK}
Line Step Recovery ^{2, 4}		V _{IN} - 100 to 400	-	300	500	-	300	500	μSec
Turn On Delay	±V _{OUT}	V _{IN} = 0V to 28V	-	-	20	-	-	20	mSec
Turn On Overshoot		VIN - UV 10 20V	-	-	25	-	-	50	mV_{PK}

- Notes: 1. Dependant on output voltage.
 - 2. Time for output voltage to settle within 1% of its nominal value.
 - 3. Derate linearly to 0 at 135°C.
 - 4. Verified by qualification testing.

 - 5. Half load at +V_{OUT} and half load at -V_{OUT}.
 6. Up to 70% of the total power or current can be drawn from any one of the two outputs.
 - 7. 5% Load to Full Load at -55°C.



SPECIFICATIONS ($T_{CASE} = -55^{\circ}C$ to $+125^{\circ}C$, $V_{IN} = +28V \pm 5\%$, Full Load⁵, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS

 $\begin{array}{lll} \mbox{Input Voltage (Continuous)} & 50 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)} & 80 \ \mbox{Volts} \\ \mbox{Output Power}^1 & 40 \ \mbox{Watts} \\ \mbox{Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}C$)} & 13 \ \mbox{Watts} \\ \end{array}$

Junction Temperature Rise to Case +15°C
Storage Temperature -65°C to +150°C

Lead Solder Temperature (10 seconds) 270°C

Weight (Maximum) (Un-Flanged / Flanged) (49 / 52) Grams

Parameter		Conditions	I	OVTR2815I)	Units
		Conditions	Min	Тур	Max	Omis
STATIC						
INPUT .		Continuous	15	28	50	V
Voltage⁴		Transient, 1 sec	-	-	80	V
Current		Inhibited	-	-	6	mA
Current		No Load	-	-	90	mA
Ripple Current		Full Load ⁵ , 20Hz to 10MHz	-	-	50	mA_{p-p}
Inhibit Pin Input ⁴			0	-	1.5	V
Inhibit Pin Open Circuit V	oltage ⁴		9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	V
	+V _{OUT}	T _{CASE} = 25°C	14.85	15.0	15.15	V
OUTPUT	$+V_{OUT}$	T _{CASE} = -55°C to +125°C	14.70	15.0	15.30	V
Voltage ⁵	$-V_{OUT}$	T _{CASE} = 25°C	14.70	15.0	15.30	V
	$-V_{OUT}$	T _{CASE} = -55°C to +125°C	14.55	15.0	15.45	V
Power ^{3,6}	Total		-	-	40	W
rowei	$\pm V_{\text{OUT}}$	Either Output	-	-	28	W
Current ^{3,6}	±V _{OUT}	Either Output	-	-	1.87	Α
Ripple Voltage	$\pm V_{\text{OUT}}$	Full Load⁵, 20Hz to 10MHz	-	-	50	mV_{p-p}
Line Deculation	+V _{OUT}	V _{IN} = 16V to 40V	-	-	20	mV
Line Regulation	-V _{OUT}	V _{IN} = 16V to 40V	-	-	200	mV
Load Demulation	+V _{OUT}	No Load to Full Load ⁵	-	-	50	mV
Load Regulation	$-V_{OUT}$	No Load to Full Load ^{5,7}	-	-	200	mV
Cross Regulation	-V _{OUT}	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	650	mV
EFFICIENCY		Full Load⁵	79	-	-	%
LOAD FALLET BOWER BY	DATION	Overload ⁴	-	-	14	W
LOAD FAULT POWER DISS	PATION	Short Circuit	-	-	14	W
CAPACITIVE LOAD ⁴		Either Output	-	-	500	μF
SWITCHING FREQUENCY			400	450	550	kHz
SYNC FREQUENCY RANGE		V _H – V _L = 5V, DC = 20-80%	500	-	600	kHz
ISOLATION		500 V _{DC} , T _{CASE} = 25°C	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T _C = 55°C	-	413	-	kHrs

See notes next page.



SPECIFICATIONS (T_{CASE} = -55°C to +125°C, V_{IN} = +28V ± 5%, Full Load⁵, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS							
Input Voltage (Continuous)	50 V _{DC}	Junction Temperature Rise to Case	+15°C				
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C				
Output Power ¹	40 Watts	Lead Solder Temperature (10 seconds)	270°C				
Power Dissipation (Full Load, T _{CASE} = +125°C)	13 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(49 / 52) Grams				

Parameter		Conditions	ı	Units		
		Conditions	Min	Тур	Max	Onits
DYNAMIC						
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	500	mV_{PK}
Load Step Recovery ²		riali Load to r dii Load	-	-	300	μSec
Line Step Output Transient ⁴	±V _{OUT}	V _{IN} = 16V to 40V	-	500	900	mV_{PK}
Line Step Recovery ^{2, 4}		V _{IN} = 16V to 40V	-	300	500	μSec
Turn On Delay	±V _{OUT}	\/ = 0\/ to 20\/	-	-	20	mSec
Turn On Overshoot	•	$V_{IN} = 0V \text{ to } 28V$	-	-	50	mV_{PK}

- Notes: 1. Dependant on output voltage.
 - 2. Time for output voltage to settle within 1% of its nominal value.
 - 3. Derate linearly to 0 at 135°C.
 - 4. Verified by qualification testing.

 - 5. Half load at +V_{OUT} and half load at -V_{OUT}.
 6. Up to 70% of the total power or current can be drawn from any one of the two outputs.
 - 7. 5% Load to Full Load at -55°C.



BLOCK DIAGRAM

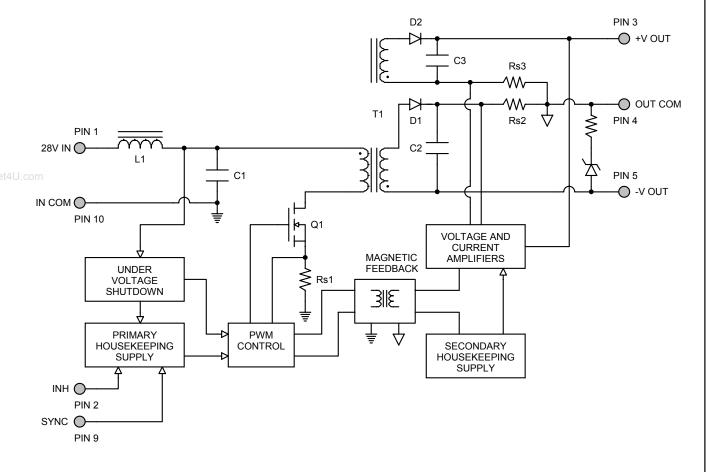


Figure 2

CONNECTION DIAGRAM

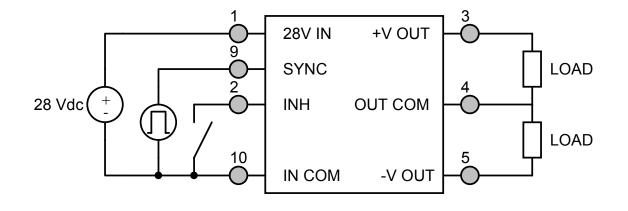


Figure 3



INHIBIT DRIVE CONNECTION DIAGRAMS

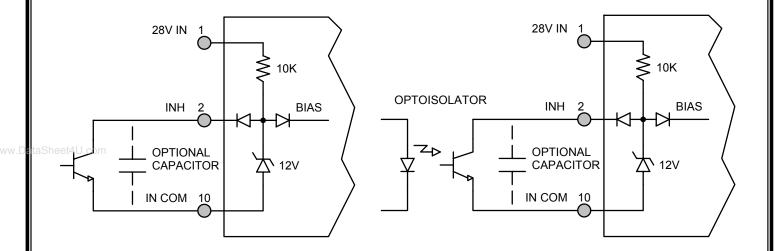


Figure 4 – Internal Inhibit Circuit and Recommended Drive (Shown with optional capacitor for turn-on delay)

Figure 5 – Isolated Inhibit Drive
(Shown with optional capacitor for turn-on delay)

EMI FILTER HOOKUP DIAGRAM

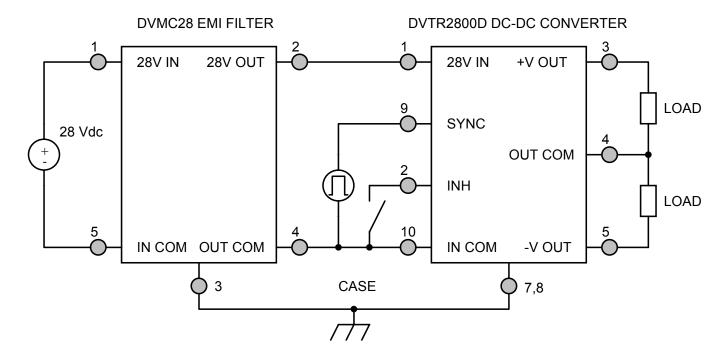
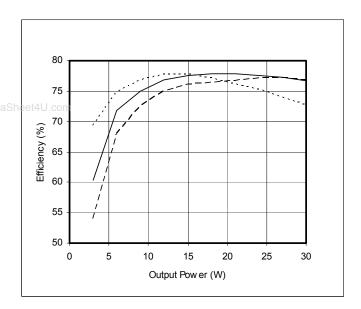


Figure 6 - Converter with EMI Filter



EFFICIENCY PERFORMANCE CURVES (T_{CASE} = 25°C, Full Load, Unless Otherwise Specified)



85 80 (%) 75 65 60 0 10 20 30 40 Output Pow er (W)

Figure 7 – DVTR2805D Efficiency (%) vs. Output Power (W)

Figure 8 – DVTR2812D Efficiency (%) vs. Output Power (W)

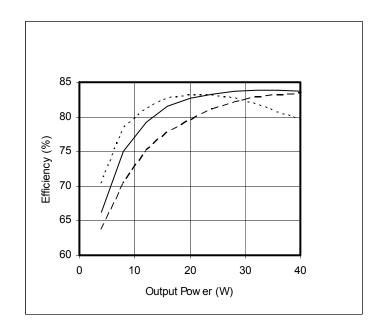


Figure 9 – DVTR2815D Efficiency (%) vs. Output Power (W)



EMI PERFORMANCE CURVES

(T_{CASE} = 25°C, V_{IN} = +28V ± 5%, Full Load, Unless Otherwise Specified)

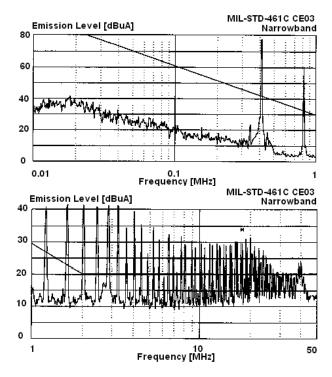


Figure 10 - DVTR2800D without EMI Filter

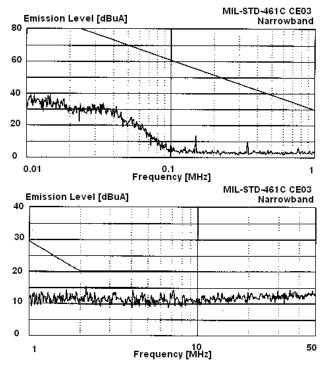
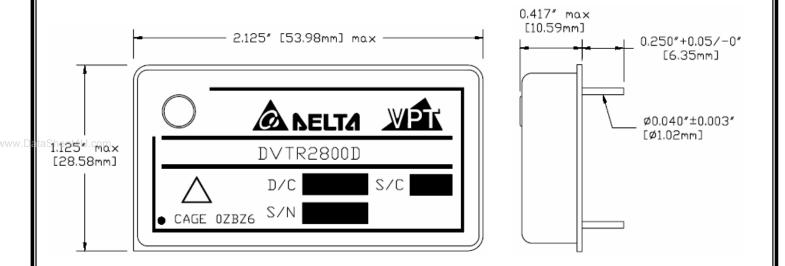


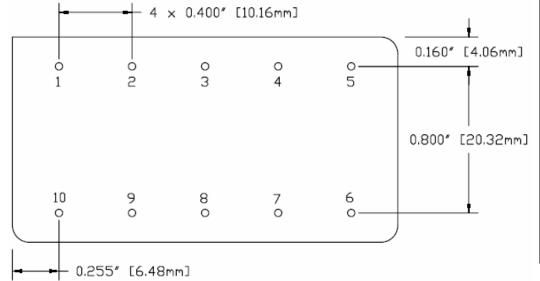
Figure 11 – DVTR2800D with EMI Filter



PACKAGE SPECIFICATIONS (NON-FLANGED, SOLDER SEAL)



TOP VIEW SIDE VIEW



PIN	FUNCTION
1	28V IN
2	INHIBIT
3	+V OUT
4	OUT COM
5	-V OUT
6	CASE
7	CASE
8	CASE
9	SYNC
10	IN COM

BOTTOM VIEW

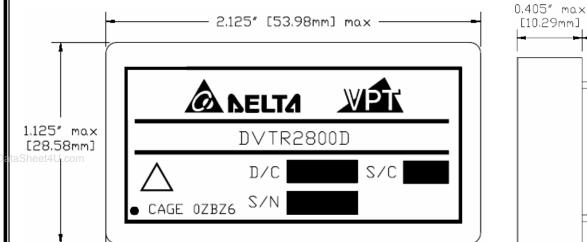
Figure 12 – Non-Flanged, Solder Seal Tin Plated Package and Pinout (Not Used for /HB or Higher Screened Products) (Dimensional Limits are ±0.005" Unless Otherwise Stated)

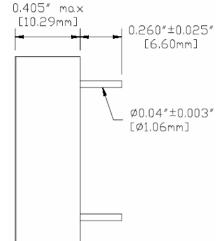
www.DataSheet4U.com

1X008DSF 10



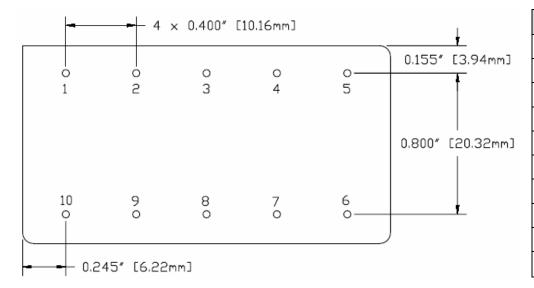
PACKAGE SPECIFICATIONS (NON-FLANGED, SEAM SEAL)





SIDE VIEW

TOP VIEW



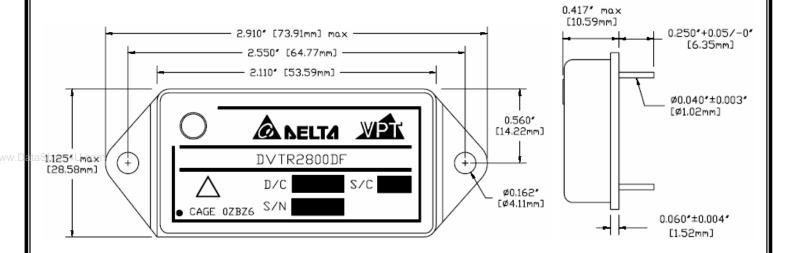
PIN	FUNCTION
1	28V IN
2	INHIBIT
3	+V OUT
4	OUT COM
5	-V OUT
6	CASE
7	CASE
8	CASE
9	SYNC
10	IN COM

BOTTOM VIEW

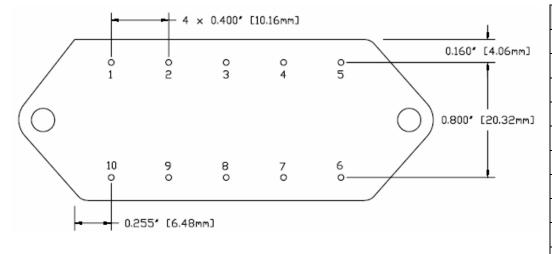
Figure 13 – Non-Flanged, Seam Seal Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)



PACKAGE SPECIFICATIONS (FLANGED, SOLDER SEAL)



TOP VIEW SIDE VIEW



PIN	FUNCTION
1	28V IN
2	INHIBIT
3	+V OUT
4	OUT COM
5	-V OUT
6	CASE
7	CASE
8	CASE
9	SYNC
10	IN COM

BOTTOM VIEW

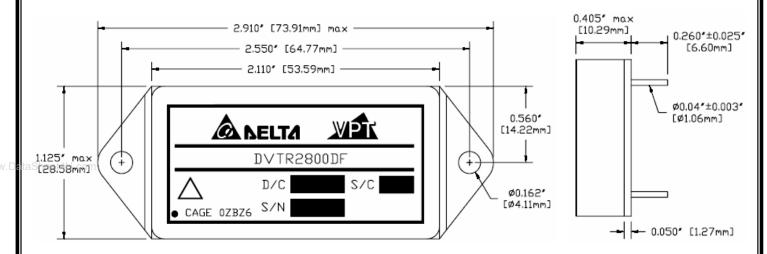
Figure 14 – Flanged, Solder Seal Tin Plated Package and Pinout (Not Used for /HB or Higher Screened Products) (Dimensional Limits are ±0.005" Unless Otherwise Stated)

www.DataSheet4U.com

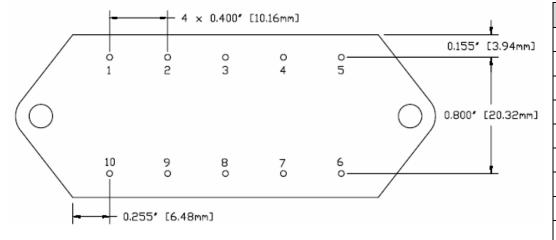
1X008DSF 12



PACKAGE SPECIFICATIONS (FLANGED, SEAM SEAL)



TOP VIEW SIDE VIEW



PIN	FUNCTION
1	28V IN
2	INHIBIT
3	+V OUT
4	OUT COM
5	-V OUT
6	CASE
7	CASE
8	CASE
9	SYNC
10	IN COM

BOTTOM VIEW

Figure 15 – Flanged, Seam Seal Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)



PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	28V IN	Positive Input Voltage Connection
2	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown. Logic High = Enabled Output. Unconnected or open collector TTL.
3	+V OUT	Positive Output Voltage Connection
eet4U.com	OUT COM	Output Common Connection
5	-V OUT	Negative Output Voltage Connection
6	CASE	Case Connection
7	CASE	Case Connection
8	CASE	Case Connection
9	SYNC	Synchronization Signal
10	IN COM	Input Common Connection



ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

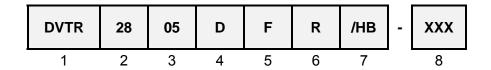
Screening	MIL-STD-883	Standard (No Suffix)	Extended /ES	HB /HB	Class H /H	Class K /K
Non- Destructive Bond Pull	Method 2023	•	•	•	•	•
Internal Visual	Method 2017, 2032 Internal Procedure	•	•	•	•	•
Temperature Cycling	Method 1010, Condition C Method 1010, -55°C to 125°C		•	•	•	•
Constant Acceleration	Method 2001, 3000g, Y1 Direction Method 2001, 500g, Y1 Direction		•	•	•	•
PIND	Method 2020, Condition A ²					•
Pre Burn-In Electrical	100% at 25°C					•
Burn-In	Method 1015, 320 hours at +125°C Method 1015, 160 hours at +125°C 96 hours at +125°C 24 hours at +125°C	•	•	•	•	•
Final Electrical	MIL-PRF-38534, Group A ¹ 100% at 25°C	•	•	•	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 ⁻³)	•	•	•	•	•
Radiography	Method 2012 ³					•
External Visual	Method 2009	•	•	•	•	•

Notes:

- 100% R&R testing at -55° C, $+25^{\circ}$ C, and $+125^{\circ}$ C with all test data included in product shipment. PIND test Certificate of Compliance included in product shipment. 1.
- 2.
- Radiographic test Certificate of Compliance and film(s) included in product shipment. 3.



ORDERING INFORMATION



(1) (2) (3)

Product Series	Nominal Input Voltage		Output Voltage		Number of Outputs	
DVTR	28	28 Volts	05 12 15	± 5 Volts ± 12 Volts ± 15 Volts	D	Dual

(5) (6) (7)

Packa	ge Option	Rad-Hard Option ²		Screening Code ^{1,3}		Additional Screening Code	
None F	Non-Flanged Flanged	None R	Standard 100 kRad	None /ES /HB /H /K	Standard Extended HB Class H Class K	Contact Sales	

Notes: 1. Contact the VPT Inc. Sales Department for availability of Class H (/H) or Class K (/K) qualified products.

2. VPT Inc. is not currently qualified to a DSCC certified radiation hardness assurance program.

3. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.



SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	DVTR2800D Series Similar Part Number		
*T.B.D.	DVTR2805D/H DVTR2805DF/H		
*T.B.D.	DVTR2812D/H DVTR2812DF/H		
*T.B.D.	DVTR2815D/H DVTR2815DF/H		

401100110.00111

Do not use the DVTR2800D Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DSCC website at http://www.dscc.dla.mil/programs/smcr/. The SMD number listed above is for MIL-PRF-38534 Class H screening, standard gold plated lead finish, and no RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels.

CONTACT INFORMATION

To request a quotation or place an order please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 353-3010 **Fax**: (425) 353-4030

E-mail: vptsales@vpt-inc.com

All information contained in this datasheet is believed to be accurate, however, no responsibility is assumed for possible errors or omissions. The products or specifications contained herein are subject to change without notice.