

74131

1-of-8 inverting decoder/demultiplexer with address register.

		+-----+		
S0	1	+-+ 16		VCC
S1	2			/Y0
S2	3			/Y1
CLK	4	74		/Y2
/EN2	5	131		/Y3
EN1	6			/Y4
/Y7	7			/Y5
GND	8			/Y6
		+-----+		

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