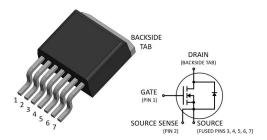
MSC060SMA070SA

700 V 60 mΩ SiC N-Channel Power MOSFET

Product Overview

The silicon carbide (SiC) power MOSFET product line from Microchip increases the performance over silicon MOSFET and silicon IGBT solutions while lowering the total cost of ownership for high-voltage applications. The MSC060SMA070SA device is a 700 V, 60 m Ω SiC MOSFET in a TO-263 7-lead package with a source sense.



Features

The following are key features of the MSC060SMA070SA device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T_{J(max)} = 175 °C
- · Fast and reliable body diode
- · Superior avalanche ruggedness
- RoHS compliant

Benefits

The following are benefits of the MSC060SMA070SA device:

- High efficiency to enable lighter, more compact system
- · Simple to drive and easy to parallel
- · Improved thermal capabilities and lower switching losses
- · Eliminates the need for external freewheeling diode
- Lower system cost of ownership

Applications

The MSC060SMA070SA device is designed for the following applications:

- · PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution

1. Device Specifications

This section shows the specifications of the MSC060SMA070SA device.

1.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings of the MSC060SMA070SA device.

Table 1-1. Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain source voltage	700	V
I _D	Continuous drain current at T _C = 25 °C	48	Α
	Continuous drain current at T _C = 100 °C	34	
I _{DM}	Pulsed drain current ¹	100	
V _{GS}	Gate-source voltage	23 to -10	V
P _D	Total power dissipation at T _C = 25 °C	208	W
	Linear derating factor	1.39	W/°C

Note:

The following table shows the thermal and mechanical characteristics of the MSC060SMA070SA device.

Table 1-2. Thermal and Mechanical Characteristics

Symbol	Characteristic/Test Conditions	Min	Тур	Max	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance		0.48	0.72	°C/W
T _J	Operating junction temperature	- 55		175	°C
T _{STG}	Storage temperature	- 55		150	°C
	Reflow temperature			260	°C
Wt	Package weight		0.05		oz
			1.42		g

1.2 Electrical Performance

The following table shows the static characteristics of the MSC060SMA070SA device. T_J = 25 °C unless otherwise specified.

Table 1-3. Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	V _{GS} = 0 V, I _D = 100 μA	700			V
R _{DS(on)}	Drain-source on resistance ¹	V _{GS} = 20 V, I _D = 20 A		60	75	mΩ
V _{GS(th)}	Gate-source threshold voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	1.9	2.4		V

^{1.} Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

continued						
Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I _{DSS}	Zero gate voltage drain current	V _{DS} = 700 V, V _{GS} = 0 V			100	μΑ
		V _{DS} = 700 V, V _{GS} = 0 V, T _J = 125 °C			500	
I _{GSS}	Gate-source leakage current	V _{GS} = 20 V/–10 V			±100	nA

Note:

1. Pulse test: pulse width < 380 μs, duty cycle < 2%.

The following table shows the dynamic characteristics of the MSC060SMA070SA device. T_J = 25 °C unless otherwise specified.

Table 1-4. Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input capacitance	V _{GS} = 0 V, V _{DD} = 700 V, V _{AC} = 25		1175		pF
C _{rss}	Reverse transfer capacitance	mV, f = 1 MHz		8		
C _{oss}	Output capacitance			130		
Qg	Total gate charge	$V_{GS} = -5 \text{ V/20 V}, V_{DD} = 470 \text{ V}, I_{D} =$		56		nC
Q _{gs}	Gate-source charge	20 A		15		
Q _{gd}	Gate-drain charge			9		
t _{d(on)}	Turn-on delay time	$V_{DD} = 470 \text{ V}, V_{GS} = -5 \text{ V}/20 \text{ V}, I_{D}$		14		ns
t _r	Voltage rise time	= 20 A, $R_{g(ext)}$ = 8 Ω , Freewheeling diode = MSC060SMA070SA (V_{GS}		11		
t _{d(off)}	Turn-off delay time	= -5 V); reference Fig. 1-16		22		
t _f	Voltage fall time			9		
E _{on}	Turn-on switching energy			125		μJ
E _{off}	Turn-off switching energy			19		
ESR	Gate equivalent series resistance	f = 1 MHz, 25 mV, drain short		2.6		Ω
SCWT	Short circuit withstand time	V _{DS} = 560 V, V _{GS} = 20 V		3		μs
E _{AS}	Avalanche energy, single pulse	V _{DS} = 150 V, I _D = 20 A		1150		mJ

The following table shows the body diode characteristics of the MSC060SMA070SA device. T_J = 25 °C unless otherwise specified.

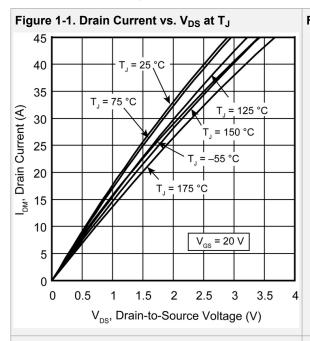
Table 1-5. Body Diode Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V _{SD}	Diode forward voltage	I _{SD} = 20 A, V _{GS} = 0 V		4.0		V
		$I_{SD} = 20 \text{ A}, V_{GS} = -5 \text{ V}$		4.2		

continued							
Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit	
t _{rr}	Reverse recovery time	$I_{SD} = 20 \text{ A}, V_{GS} = -5 \text{ V}, V_{DD}$		11		ns	
Q _{rr}	Reverse recovery charge	= 470 V, dl/dt = -10000 A/ μ s, Drive Rg = 8 Ω		404		nC	
I _{RRM}	Reverse recovery current			64		Α	

1.3 Typical Performance Curves

This section shows the typical performance curves of the MSC060SMA070SA device.



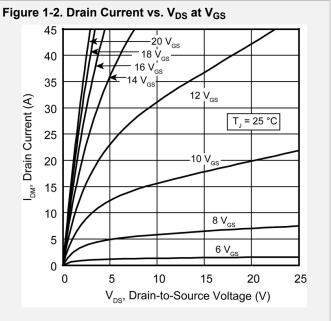
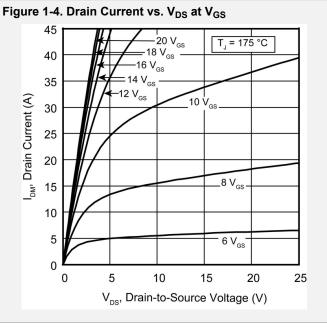
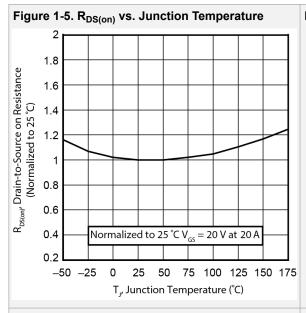


Figure 1-3. Drain Current vs. V_{DS} at V_{GS} 20 V_{GS} T, = 150 °C 40 35 Drain Current (A) 10 V_{GS} 8 V_{gs} 10 5 $6~\rm V_{\rm GS}$ 0 0 10 15 25 V_{DS}, Drain-to-Source Voltage (V)





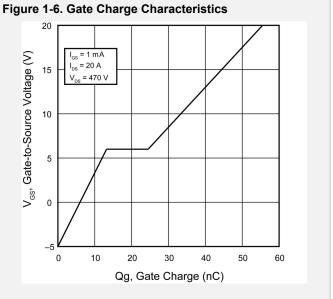


Figure 1-7. Capacitance vs. Drain-to-Source Voltage

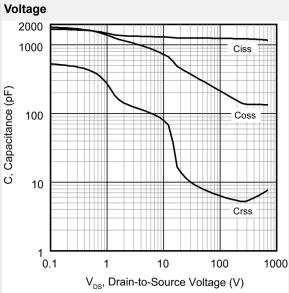
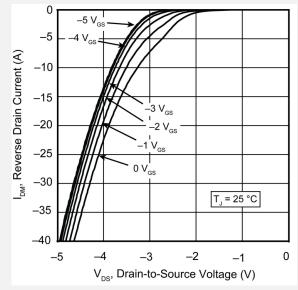
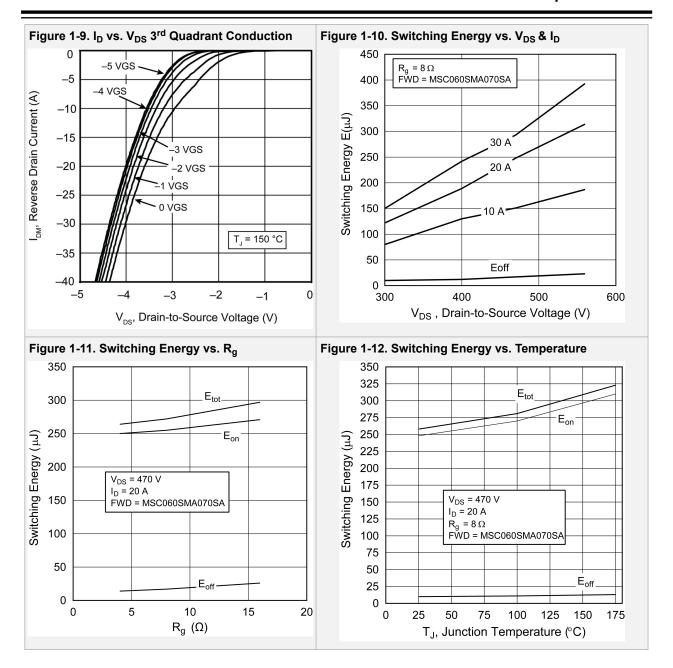


Figure 1-8. I_D vs. V_{DS} 3rd Quadrant Conduction





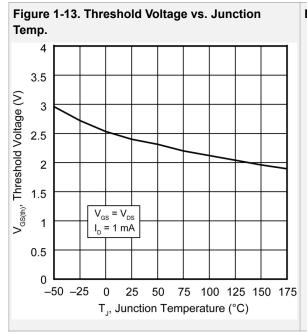


Figure 1-14. Forward Safe Operating Area

100

(V) tuesday 10 ps

100

T_c = 100 °C 10 ms

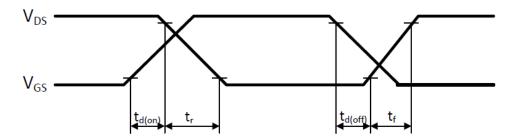
100 ps

10

Figure 1-15. Maximum Transient Thermal Impedance 1 D = 0.9 0.7 Z_{OJC}, Thermal Impedance (C/W) 0.3 0.1 Note: 0.05 PDM Single Pulse Duty Factor D = $^{t_1}/t_2$ $Peak T_{J} = P_{DM} \times Z_{\emptyset JC} + T_{C}$ 0.01 1×10⁻⁶ 1×10⁻⁵ 1×10⁻⁴ 1×10⁻³ 1×10⁻² 1×10⁻¹ 1×100 Pulse Duration (seconds)

The following figure shows the switching waveform diagram of the MSC060SMA070SA device.

Figure 1-16. Switching Waveform



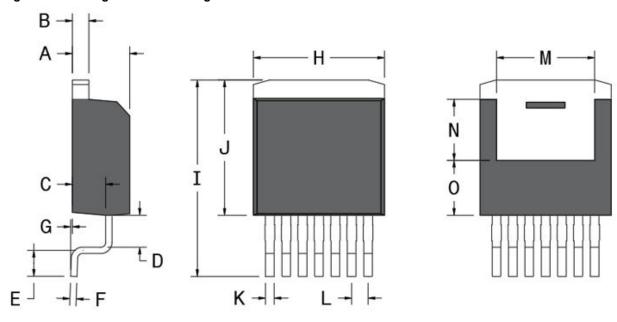
2. Package Specification

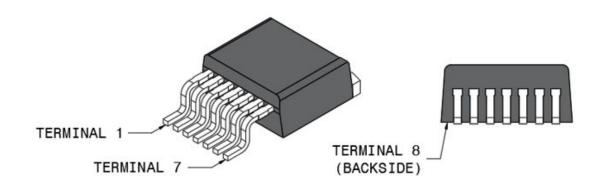
This section shows the package specification of the MSC060SMA070SA device.

2.1 Package Outline Drawing

The following figure illustrates the TO-263 7-lead package outline of the MSC060SMA070SA device.

Figure 2-1. Package Outline Drawing





The following table shows the TO-263 7-lead dimensions and should be used in conjunction with the package outline drawing.

Table 2-1. TO-263 Dimensions

Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)
Α	4.30	4.57	0.169	0.180
В	1.17	1.40	0.046	0.055
С	2.50	2.70	0.098	0.106

MSC060SMA070SA

Package Specification

continued							
Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)			
D	2.85	2.95	0.112	0.116			
E	2.32	2.70	0.091	0.106			
F	0.33	0.65	0.013	0.026			
G	0.00	0.25	0.00	0.010			
Н	10.13	10.23	0.399	0.403			
ı	15.04	17.12	0.592	0.674			
J	10.19	10.80	0.401	0.425			
K	0.50	0.70	0.020	0.028			
L	1.27 BSC		0.050 BSC				
M	6.78	7.67	0.267	0.302			
N	4.66	4.81	0.183	0.189			
0	4.20	4.30	0.165	0.169			
Terminal 1	Gate						
Terminal 2	Source sense						
Terminal 3	Source						
Terminal 4	Source	Source					
Terminal 5	Source						
Terminal 6	Source						
Terminal 7	Source						
Terminal 8	Drain						

3. Revision History

Table 3-1. Revision History

Revision	Date	Description
Α	03/2022	Document created.

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