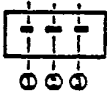
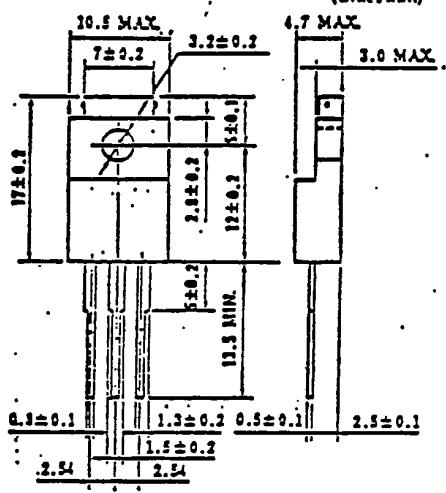


FAST SWITCHING N-CHANNEL SILICON POWER MOS FET

PACKAGE DIMENSIONS (Unit: mm)



1. Gate
2. Drain
3. Source

Features

- Suitable for switching power supplies, actuator controls and pulse circuits
- 4V Gate Drive — Logic level —
- Large Current Switching : $I_D(DC)=12A$
- Low $R_{DS(on)}$
- No second breakdown

Absolute Maximum Ratings ($T_a=25^\circ C$)

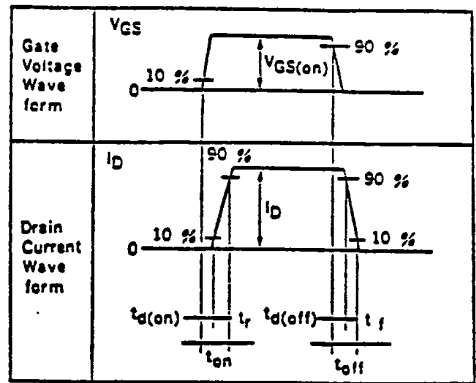
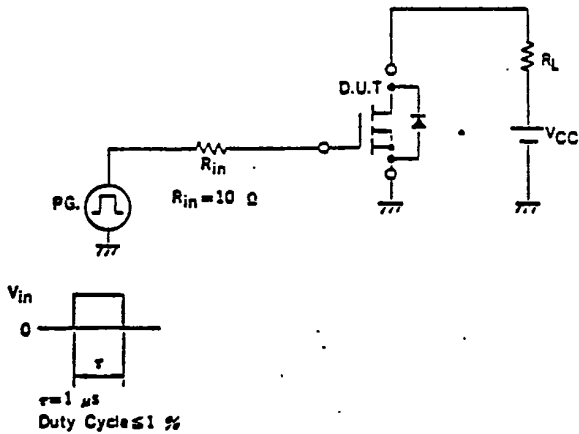
Drain to Source Voltage	V_{DS}	100V
Gate to Source Voltage	V_{GS}	$\pm 20V$
Continuous Drain Current	$I_D(DC)$	$\pm 12A$
Pulse Drain Current	$I_D(pulse)$	* $\pm 48A$
Total Power Dissipation	P_T	2.0W
Total Power Dissipation	P_T^{**}	35W
Channel Temperature	T_{ch}	150 °C
Storage Temperature	T_{stg}	-55to+150 °C
	* $T_{ch} \leq 150^\circ C$	
	** $T_c=25^\circ C$	

Electrical Characteristics ($T_a=25^\circ C$)

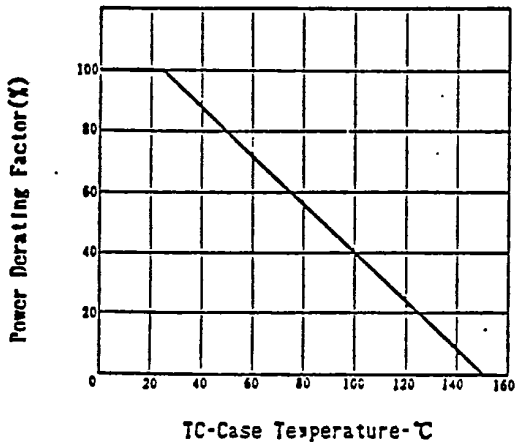
Characteristics	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain Leakage Current	I_{DSS}			10	μA	$V_{DS}=100V, V_{GS}=0$
Gate to Source Leakage Current	I_{GSS}			100	nA	$V_{GS}=20V, V_{DS}=0$
Gate to Source Cutoff Voltage	$V_{GS(off)}$	1.0		2.5	V	$V_{DS}=10V, I_D=1.0mA$
Forward Transfer Admittance	$ y_{fs} $	4.0	10		S	$V_{DS}=10V, I_D=8.0A$
Drain to Source On-State Resistance	$R_{DS(on)}$		0.1	0.18	Ω	$V_{GS}=10V, I_D=8.0A$
Drain to Source On-State Resistance	$R_{DS(on)}$		0.15	0.25	Ω	$V_{GS}=4.0V, I_D=8.0A$
Input Capacitance	C_{iss}		1200		pF	$V_{DS}=10V,$
Output Capacitance	C_{oss}		400		pF	$V_{GS}=0,$
Reverse Transfer Capacitance	C_{rss}		90		pF	$f=1.0MHz$
Turn-On Delay Time	$t_d(on)$		10		ns	$I_D=8.0A,$
Rise Time	t_r		20		ns	$V_{GS(on)}=10V,$
Turn-Off Delay Time	$t_d(off)$		65		ns	$V_{cc}=40V,$
Fall Time	t_f		55		ns	$R_L=5.0\Omega$

NEC cannot assume any responsibility for any circuits shown or represent that they are free from patent infringement

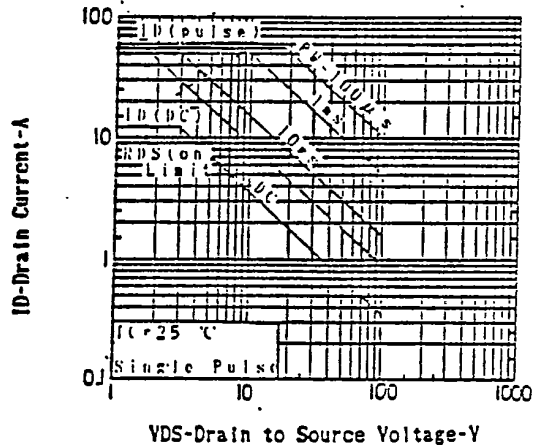
TURN-ON AND TURN-OFF TIME TEST CIRCUIT



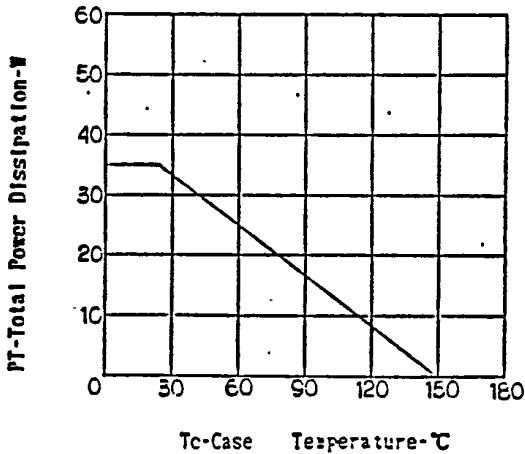
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



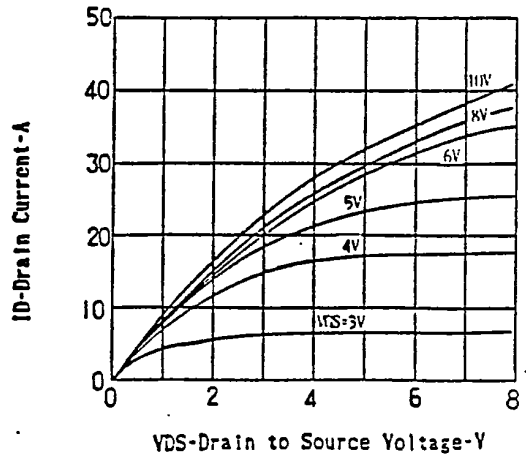
FORWARD BIAS SAFE OPERATING AREA



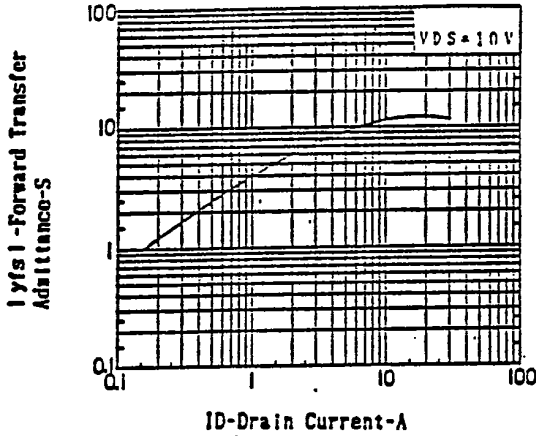
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



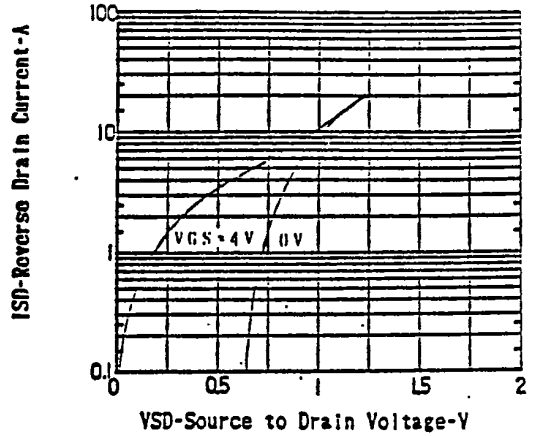
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



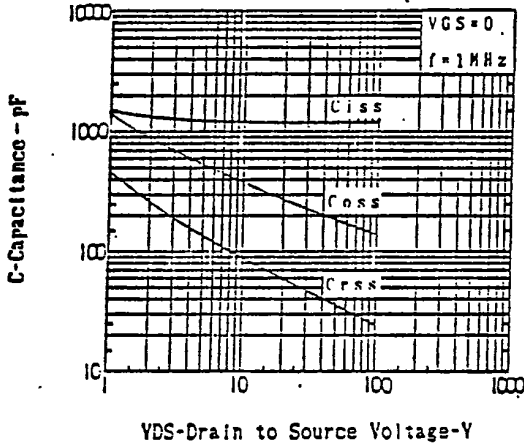
FORWARD TRANSFER ADMITTANCE
vs. DRAIN CURRENT



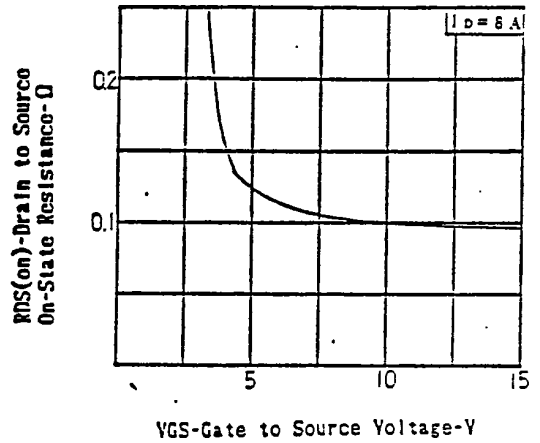
SOURCE TO DRAIN DIODE
FORWARD VOLTAGE



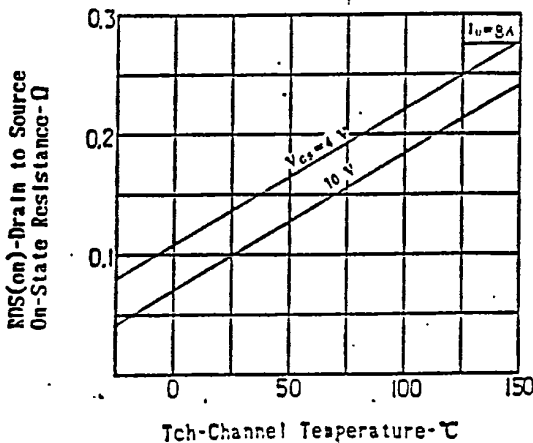
CAPACITANCE vs. DRAIN TO
SOURCE VOLTAGE



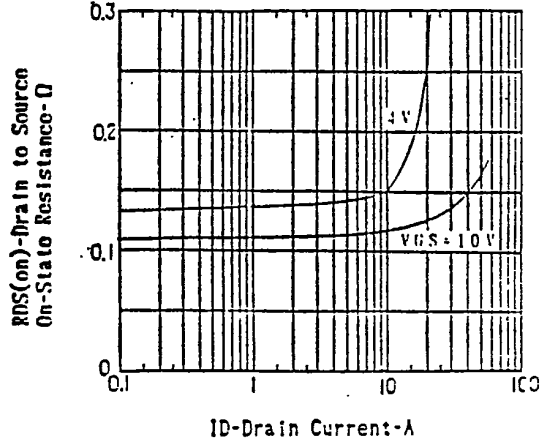
DRAIN TO SOURCE ON-STATE RESISTANCE
vs. GATE TO SOURCE VOLTAGE



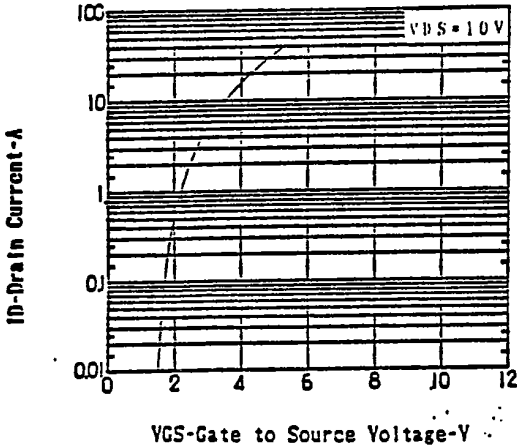
DRAIN TO SOURCE ON-STATE RESISTANCE
vs. CHANNEL TEMPERATURE



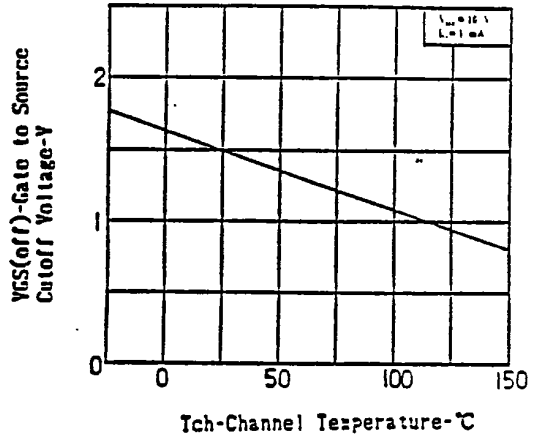
DRAIN TO SOURCE ON-STATE RESISTANCE
vs. DRAIN CURRENT



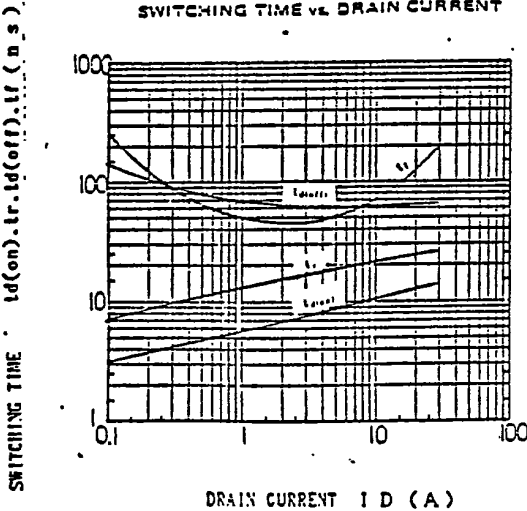
TRANSFER CHARACTERISTICS



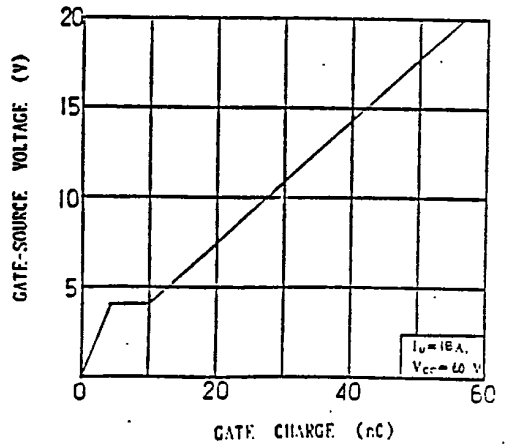
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE



SWITCHING TIME vs. DRAIN CURRENT



GATE CHARGE VS GATE-SOURCE VOLTAGE



NORMARIZED TRANSIENT THERMAL IMPEDANCE vs. PULSE WIDTH

